

## VND5E050J evaluation board

Data brief — production data

### Features

| Parameter                | Symbol     | Value     | Unit          |
|--------------------------|------------|-----------|---------------|
| Max supply voltage       | $V_{CC}$   | 41        | V             |
| Operating voltage range  | $V_{CC}$   | 4.5 to 28 | V             |
| Max On-State resistance  | $R_{ON}$   | 50        | m $\Omega$    |
| Current limitation (typ) | $I_{LIMH}$ | 27        | A             |
| Off state supply current | $I_S$      | 2         | $\mu A^{(1)}$ |

1. Typical value with all loads connected.

- Simple single IC application board dedicated for VND5E050J-E
- Provides thermal heat-sinking for ease of use in prototyping
- Provides electrical connectivity for easy prototyping

### Description

EV-VND5E050J provides you an easy way to connect ST's surface mounted VIPower® drivers into your existing prototype circuitry. This evaluation board comes pre-assembled with VND5E050J-E high-side driver.

The VND5E050J-E is a double channel high-side driver manufactured using ST proprietary VIPower M0-5 technology and housed in PowerSSO-12 package. The VND5E050J-E is designed to drive automotive grounded loads, providing protection, diagnostics and easy 3 V and 5 V CMOS-compatible interface with any microcontroller.

The device integrates advanced protective functions such as load current limitation, inrush and overload active management by power limitation, overtemperature shut-off with auto-restart and overvoltage active clamp. A dedicated analog current sense pin is associated with every

output channel provides enhanced diagnostic functions including fast detection of overload and short-circuit to ground, overtemperature indication, short-circuit to  $V_{CC}$  diagnosis and ON-state and OFF-state open-load detection.

The diagnostic feedback of the whole device can be disabled by pulling the STAT\_DIS pin up, thus allowing wired-ORing with similar devices.

Figure 1. VND5E050J evaluation board

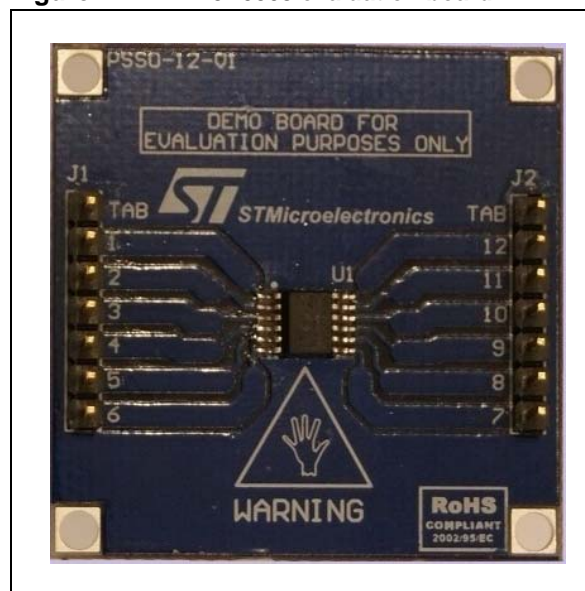


Table 1. Device summary

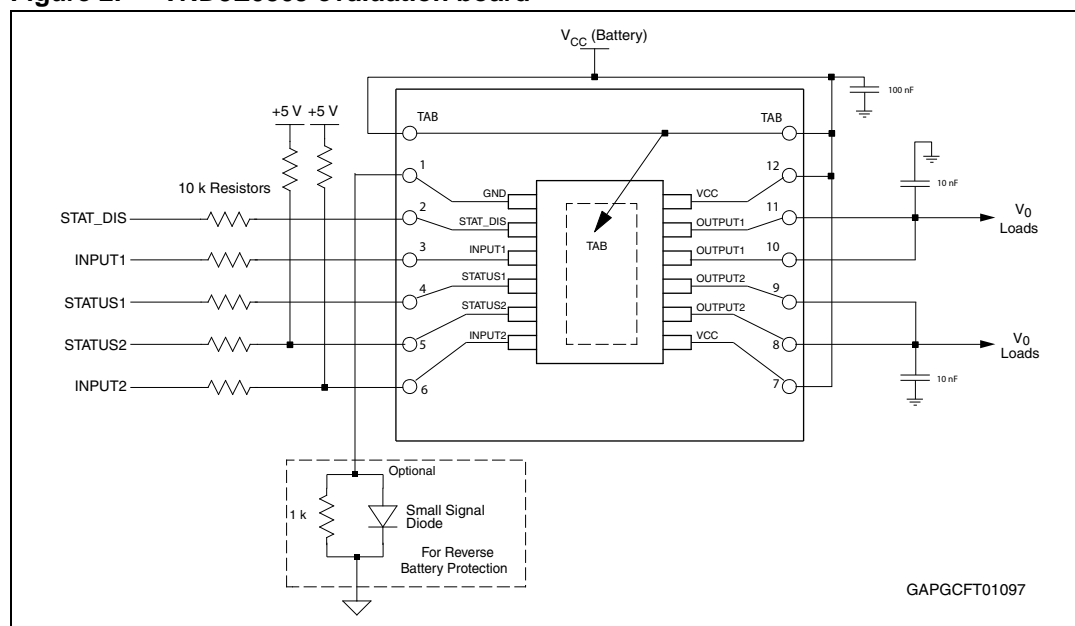
| Order code   | Reference                  |
|--------------|----------------------------|
| EV-VND5E050J | VND5E050J evaluation board |

# 1 Design recommendations

This evaluation board provides mounting solution and some heat sinking capability for prototype development, but there are still external components that are required to make these devices work in any application. For further information on how the evaluation board has to be used you can refer to the AN4210 (see [Appendix A: Reference documents](#)).

[Figure 2](#) illustrates the necessary components for any application.

**Figure 2. VND5E050J evaluation board**



ST has produced a user manual for safe designs using ST's VIPower devices. This is UM1556 (see [Appendix A: Reference documents](#)). UM1556 is a VIPower Hardware design guide that provides all necessary information to successfully design your circuit using our VIPower drivers.

All designs have different needs and requirements. Whatever design you decide to use, it will still need to be verified in order to meet your application specifications. ST implies no guarantee or warranty (see [Appendix A: Reference documents](#)).

## 2 Thermal data

**Table 2. VND5E050J-E thermal data**

| Symbol        | Parameter                                 | Max. value | Unit |
|---------------|---|------------|------|
| $R_{thj-amb}$ | Thermal resistance junction-ambient (MAX) | 31         | °C/W |

**Table 3. PCB specifications**

| Parameter               | Value       | Unit |
|-------------------------|-------------|------|
| Board dimensions        | 38 x 38     | mm   |
| Number of Cu layer      | 2           | —    |
| Layer Cu thickness      | 70          | μm   |
| Board finish thickness  | 1.6 +/- 10% | mm   |
| Board Material          | FR4         | —    |
| Thermal vias separation | 1.2         | mm   |
| Thermal vias diameter   | 0.3 /- 0.08 | mm   |

### 3 Board connector reference

Figure 3. Board layout

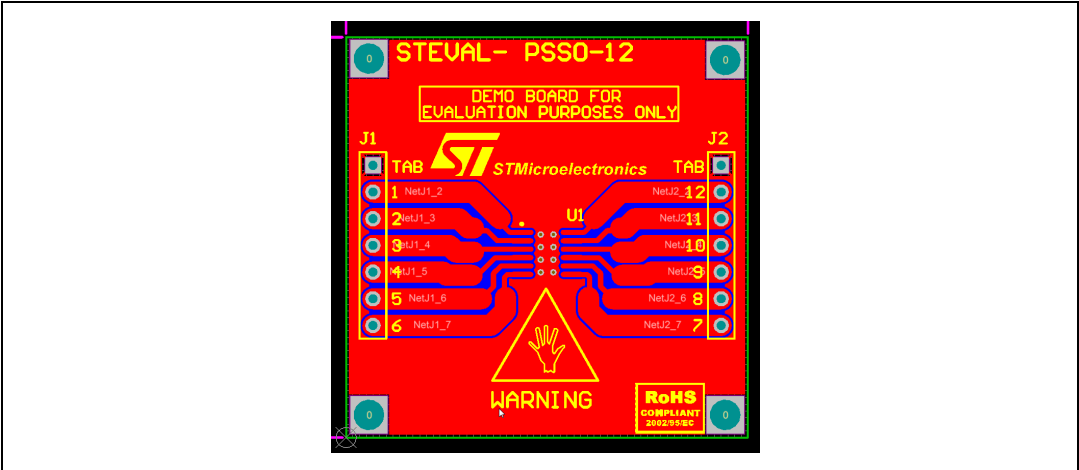


Table 4. Board connector specification

| Connector | Board lead number | Device pin function <sup>(1)</sup> |
|-----------|-------------------|------------------------------------|
| J1        | TAB               | V <sub>CC</sub>                    |
| J1        | 1                 | GND                                |
| J1        | 2                 | STAT_DIS                           |
| J1        | 3                 | INPUT1                             |
| J1        | 4                 | STATUS1                            |
| J1        | 5                 | STATUS2                            |
| J1        | 6                 | INPUT2                             |
| J2        | TAB               | V <sub>CC</sub>                    |
| J2        | 7                 | V <sub>CC</sub>                    |
| J2        | 8                 | OUTPUT2                            |
| J2        | 9                 | OUTPUT2                            |
| J2        | 10                | OUTPUT1                            |
| J2        | 11                | OUTPUT1                            |
| J2        | 12                | V <sub>CC</sub>                    |

1. For further clarification on pin functions please refer to the related datasheet (see [Appendix A: Reference documents](#)).

## 4 Package information

### 4.1 ECOPACK<sup>®</sup> packages

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: [www.st.com](http://www.st.com).

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## Appendix A      Reference documents

1. Double channel high side driver for automotive applications (VND5E050J-E, VND5E050K-E, DocID 14472)
2. VIPower M0-5 and M0-5Enhanced high-side drivers (UM1556, DocID 023520)
3. PowerSSO-12 devices evaluation board (AN4210, DocID 023963)
4. Evaluation Product Licence Agreement on [www.st.com](http://www.st.com)

## Revision history

**Table 5. Document revision history**

| Date        | Revision | Changes          |
|-------------|----------|------------------|
| 28-Nov-2012 | 1        | Initial release. |

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