TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

2SK2034

High Speed Switching Applications Analog Switch Applications

- High input impedance.
- Low gate threshold voltage.: $V_{th} = 0.5 \sim 1.5 \text{ V}$
- Excellent switching times: $t_{on} = 0.16 \mu s$ (typ.)

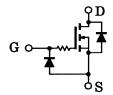
 $t_{off} = 0.15 \mu s \text{ (typ.)}$

- · Small package.
- Enhancement-mode

Marking

Equivalent Circuit

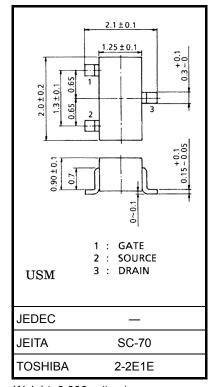




Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V_{DS}	20	V
Gate-source voltage	V_{GSS}	10	V
DC drain current	ΙD	100	mA
Drain power dissipation	P_{D}	100	mW
Channel temperature	T _{ch}	150	°C
Storage temperature range	T _{stg}	-55~150	°C

Unit: mm



Weight: 0.006 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

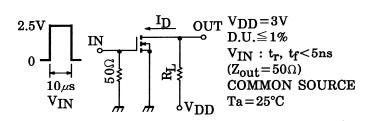
Note: This transistor is electrostatic sensitive device. Please handle with caution.

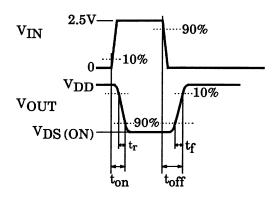


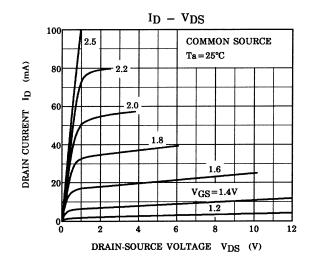
Electrical Characteristics (Ta = 25°C)

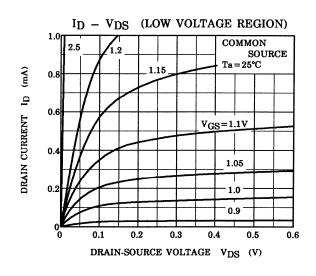
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage curre	ent	I _{GSS}	V _{GS} = 10 V, V _{DS} = 0	_	_	1	μΑ
Drain-source brea	kdown voltage	V (BR) DSS	$I_D = 100 \ \mu A, \ V_{GS} = 0$	20	_	_	V
Drain cut-off curre	nt	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0	_	_	1	μА
Gate threshold vol	tage	V _{th}	$V_{DS} = 3 \text{ V}, I_{D} = 0.1 \text{ mA}$	0.5	_	1.5	V
Forward transfer a	dmittance	Y _{fs}	$V_{DS} = 3 \text{ V}, I_D = 10 \text{ mA}$	25	50	_	mS
Drain-source ON r	esistance	R _{DS} (ON)	$I_D = 10 \text{ mA}, V_{GS} = 2.5 \text{ V}$	_	8	12	Ω
Input capacitance		C _{iss}	$V_{DS} = 3 V$, $V_{GS} = 0$, $f = 1 MHz$	_	8.5	_	pF
Reverse transfer of	apacitance	C _{rss}	$V_{DS} = 3 V$, $V_{GS} = 0$, $f = 1 MHz$	_	3.3	_	pF
Output capacitance		Coss	$V_{DS} = 3 V$, $V_{GS} = 0$, $f = 1 MHz$	_	9.3	_	pF
Switching time	Turn-on time	t _{on}	V _{DD} = 3 V, I _D = 10 mA	_	0.16	_	- μ s
			V _{GS} = 0~2.5 V				
	Turn-off time	t _{off}	V _{DD} = 3 V, I _D = 10 mA	_	0.15	_	
			V _{GS} = 0~2.5 V				

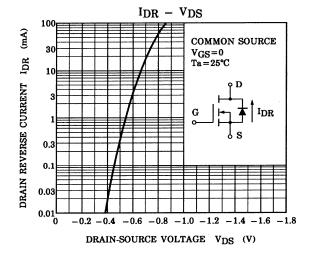
Switching Time Test Circuit

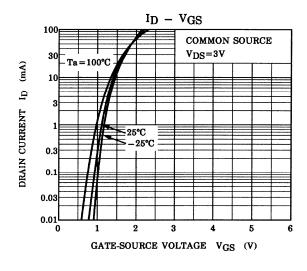


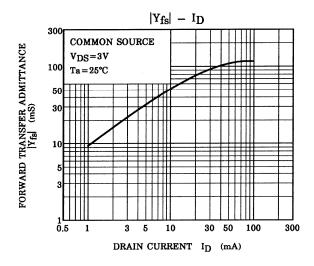


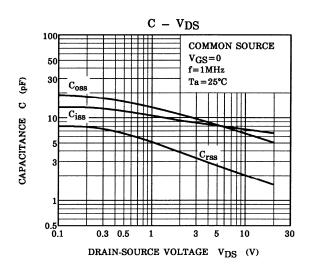


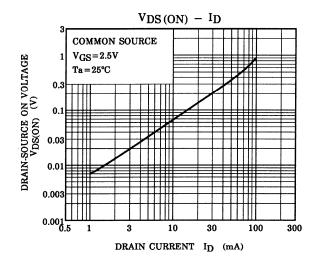


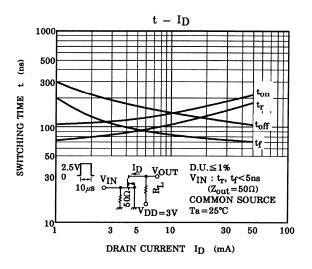


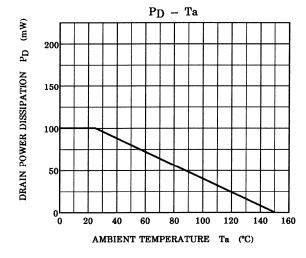












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