STP80N6F6



N-channel 60 V, 110 A STripFET™ VI DeepGATE™ Power MOSFET in TO-220 package

Datasheet - preliminary data

Features

Order codes	V _{DSS}	R _{DS(on)} max	I _D
STP80N6F6	60 V	$5.8~\text{m}\Omega$	110 A

- Low gate charge
- Very low on-resistance
- High avalanche ruggedness

Applications

■ Switching applications

Description

This device is an N-channel Power MOSFET developed using the 6th generation of STripFET™ DeepGATE™ technology, with a new gate structure. The resulting Power MOSFET exhibits the lowest RDS(on) in all packages.

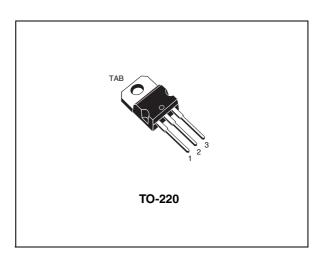


Figure 1. Internal schematic diagram

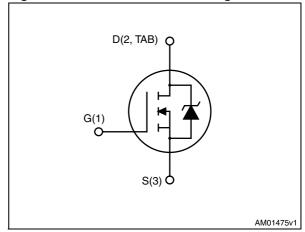


Table 1. Device summary

Order codes	Marking	Package	Packaging
STP80N6F6	80N6F6	TO-220	Tube

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STP80N6F6 Electrical ratings

1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{DS}	Drain-source voltage	60	V
V _{GS}	Gate-source voltage	± 20	V
I _D	Drain current (continuous) at T _C = 25 °C	110	Α
I _D	Drain current (continuous) at T _C = 100 °C	110	Α
I _{DM}	Drain current (pulsed)	440	Α
P _{TOT}	Total dissipation at T _C = 25 °C	150	
	Derating factor	2	W/°C
T _{stg}	Storage temperature	55 to 175	
T _j	Operating junction temperature	- 55 to 175	

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thj-case}	Thermal resistance junction-case max	1	°C/W
R _{thj-a}	Thermal resistance junction-ambient max	62.5	°C/W
T _I	Maximum lead temperature for soldering purpose	300	°C

Electrical characteristics STP80N6F6

2 Electrical characteristics

 $(T_{CASE} = 25 \, ^{\circ}C \text{ unless otherwise specified})$

Table 4. On/off states

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source breakdown voltage (V _{GS} = 0)	I _D = 250 μA	60			V
	Zero gate voltage	V _{DS} = 60 V			1	μΑ
DSS	Drain current (V _{GS} = 0)	$V_{DS} = 60 \text{ V}, T_{C} = 125 ^{\circ}\text{C}$			100	μΑ
I _{GSS}	Gate-body leakage current (V _{DS} = 0)	V _{GS} = ± 20 V			± 100	nA
V _{GS(th)}	Gate threshold voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	3		4.5	٧
R _{DS(on)}	Static drain-source on-resistance	V _{GS} = 10 V, I _D = 50 A		TBD	5.8	mΩ

Table 5. Dynamic

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C _{iss}	Input capacitance			7480		pF
C _{oss}	Output capacitance	$V_{DS} = 25 \text{ V, f} = 1 \text{ MHz,}$	_	450	_	pF
C_{rss}	Reverse transfer capacitance	V _{GS} = 0		340		рF
Q_g	Total gate charge	V 00 V 1 440 A		122		nC
Q_{gs}	Gate-source charge	$V_{DD} = 30 \text{ V}, I_{D} = 110 \text{ A},$ $V_{GS} = 10 \text{ V}$	-	TBD	-	nC
Q_{gd}	Gate-drain charge	VGS - 10 V		TBD		nC

Table 6. Switching times

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(on)}	Turn-on delay time Rise time	V _{DD} = 30 V, I _D = 50 A	-	TBD	-	ns ns
t _{d(off)}	Turn-off-delay time Fall time	$R_G = 4.7 \Omega V_{GS} = 10 V$	-	TBD	-	ns ns

Table 7. Source drain diode

Symbol	Parameter	Test conditions	Min.	Тур.	Max	Unit
I _{SD}	Source-drain current		-		110	Α
I _{SDM} ⁽¹⁾	Source-drain current (pulsed)		-		440	Α
V _{SD} ⁽²⁾	Forward on voltage	$I_{SD} = 110 \text{ A}, V_{GS} = 0$	-		1.1	V
t _{rr}	Reverse recovery time	$I_{SD} = 110 \text{ A}, V_{DD} = 48 \text{ V}$				ns
Q _{rr}	Reverse recovery charge	$di/dt = 100 A/\mu s$,	-	TBD		nC
I _{RRM}	Reverse recovery current	T _j = 150 °C				Α

^{1.} Current limited by package.

^{2.} Pulsed: pulse duration = 300 μ s, duty cycle 1.5%

3 Package mechanical data

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Table 8. TO-220 type A mechanical data

Dim		mm	
Dim.	Min.	Тур.	Max.
Α	4.40		4.60
b	0.61		0.88
b1	1.14		1.70
С	0.48		0.70
D	15.25		15.75
D1		1.27	
E	10		10.40
е	2.40		2.70
e1	4.95		5.15
F	1.23		1.32
H1	6.20		6.60
J1	2.40		2.72
L	13		14
L1	3.50		3.93
L20		16.40	
L30		28.90	
ØP	3.75		3.85
Q	2.65		2.95

Figure 2. TO-220 type A drawing

Revision history STP80N6F6

4 Revision history

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Table 9. Document revision history

Date	Revision	Changes
08-Aug-2012	1	Initial release.

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