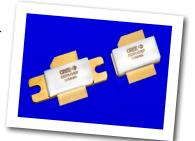


CGHV14500 500 W, 1200 - 1400 MHz, GaN HEMT for L-Band Radar Systems

Cree's CGHV14500 is a gallium nitride (GaN) high electron mobility transistor (HEMT) designed specifically with high efficiency, high gain and wide bandwidth capabilities, which makes the CGHV14500 ideal for 1.2 - 1.4 GHz L-Band radar amplifier applications. The transistor could be utilized for band specific applications ranging from UHF through 1800 MHz. The package options are ceramic/metal flange and pill package.



Package Type: 440117, 440133 PN: CGHV14500

Typical Performance Over 1.2-1.4 GHz ($T_c = 25^{\circ}c$) of Demonstration Amplifier

Parameter	1.2 GHz	1.25 GHz	1.3 GHz	1.35 GHz	1.4 GHz	Units
Output Power	505	510	510	510	510	W
Gain	17.0	17.1	17.1	17.1	17.1	dB
Drain Efficiency	70	72	70	67	67	%

Note:

Measured in the CGHV14500-TB amplifier circuit, under 500 μ s pulse width, 10% duty cycle, P_{IN} = 40 dBm.

Features

- Reference design amplifier 1.2 1.4 GHz Operation
- FET tuning range UHF through 1800 MHz
- 500 W Typical Output Power
- 17 dB Power Gain
- 70% Typical Drain Efficiency
- <0.3 dB Pulsed Amplitude Droop
- Internally pre-matched on input, unmatched output



Absolute Maximum Ratings (not simultaneous)

Parameter	Symbol	Rating	Units	Conditions
Drain-Source Voltage	V _{DSS}	125	Volts	25°C
Gate-to-Source Voltage	V _{gs}	-10, +2	Volts	25°C
Storage Temperature	T _{STG}	-65, +150	°C	
Operating Junction Temperature	T,	225	°C	
Maximum Forward Gate Current	\mathbf{I}_{GMAX}	84	mA	25°C
Maximum Drain Current ¹	I _{dmax}	36	А	25°C
Soldering Temperature ²	Τ _s	245	°C	
Screw Torque	τ	40	in-oz	
CW Thermal Resistance, Junction to Case ³	$R_{_{ ext{ hetaJC}}}$	0.47	°C/W	$P_{_{DISS}} = 334 \text{ W}, 65^{\circ}\text{C}$
Pulsed Thermal Resistance, Junction to Case ³	$R_{_{ ext{ heta}JC}}$	0.28	°C/W	$P_{\text{DISS}} = 334$ W, 500 µsec, 10%, 85°C
Pulsed Thermal Resistance, Junction to Case ⁴	$R_{_{ ext{ ext{ ext{ ext{ ext{ ext{ ext{ ext$	0.31	°C/W	$P_{_{DISS}} = 334$ W, 500 µsec, 10%, 85°C
Case Operating Temperature ⁵	T _c	-40, +130	°C	$P_{_{\text{DISS}}}$ = 334 W, 500 µsec, 10%

Note:

¹ Current limit for long term, reliable operation

² Refer to the Application Note on soldering at <u>http://www.cree.com/rf/document-library</u>

³ Measured for the CGHV14500P

 ${}^{\scriptscriptstyle 4}$ Measured for the CGHV14500F

 ${}^{\scriptscriptstyle 5}\mbox{See}$ also, the Power Dissipation De-rating Curve on Page 5

Electrical Characteristics

Characteristics	Symbol	Min.	Тур.	Max.	Units	Conditions	
DC Characteristics ¹ (T _c = 25 °C)							
Gate Threshold Voltage	$V_{\rm GS(th)}$	-3.8	-3.0	-2.3	V _{DC}	$V_{_{\rm DS}}$ = 10 V, $I_{_{\rm D}}$ = 83.6 mA	
Gate Quiescent Voltage	V _{GS(Q)}	-	-2.7	-	V _{DC}	$V_{_{\rm DS}}$ = 50 V, $I_{_{\rm D}}$ = 500 mA	
Saturated Drain Current ²	I _{DS}	62.7	75.2	-	А	$V_{_{\rm DS}}$ = 6.0 V, $V_{_{\rm GS}}$ = 2.0 V	
Drain-Source Breakdown Voltage	V _{BR}	125	-	-	V _{DC}	$V_{_{GS}}$ = -8 V, I _D = 83.6 mA	
RF Characteristics ³ ($T_c = 25^{\circ}C_r$)	F ₀ = 1.3 GHz	z unless othe	erwise notec	i)			
Output Power	P _{OUT}	422	510	-	W	$V_{_{\rm DD}}$ = 50 V, $I_{_{\rm DQ}}$ = 500 mA, $P_{_{\rm IN}}$ = 40 dBm	
Drain Efficiency	D _E	63	70	-	%	$V_{_{\rm DD}}$ = 50 V, $I_{_{\rm DQ}}$ = 500 mA, $P_{_{\rm IN}}$ = 40 dBm	
Power Gain	G _p	16.25	17.1	-	dB	$V_{_{\rm DD}}$ = 50 V, $I_{_{\rm DQ}}$ = 500 mA, $P_{_{\rm IN}}$ = 40 dBm	
Pulsed Amplitude Droop	D	-	-0.3	-	dB	$V_{_{\rm DD}}$ = 50 V, $I_{_{\rm DQ}}$ = 500 mA	
Output Mismatch Stress	VSWR	-	5:1	-	Ψ	No damage at all phase angles, $V_{_{\rm DD}}$ = 50 V, $I_{_{\rm DQ}}$ = 500 mA, $P_{_{\rm IN}}$ = 40 dBm Pulsed	

Notes:

¹ Measured on wafer prior to packaging.

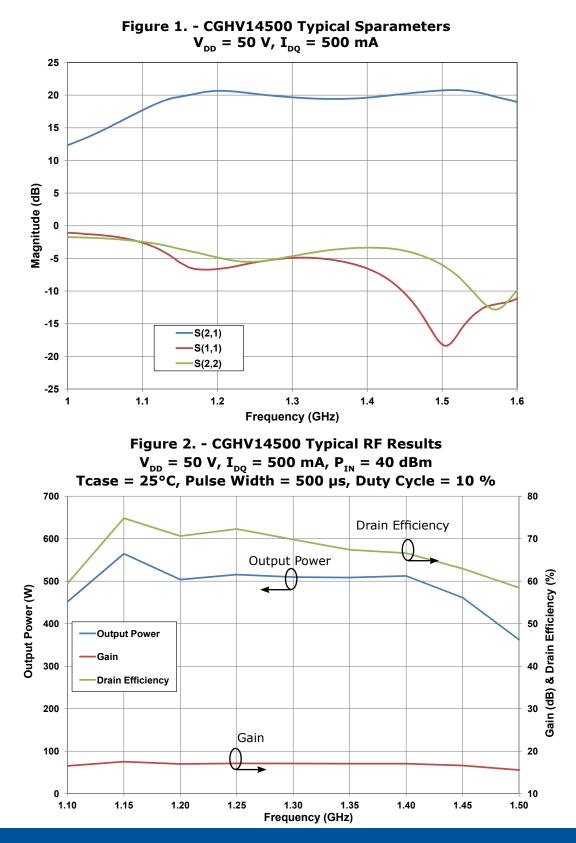
² Scaled from PCM data

 3 Measured in CGHV14500-TB. Pulse Width = 500 $\mu S,$ Duty Cycle = 10%.

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Typical Performance



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3



Typical Performance

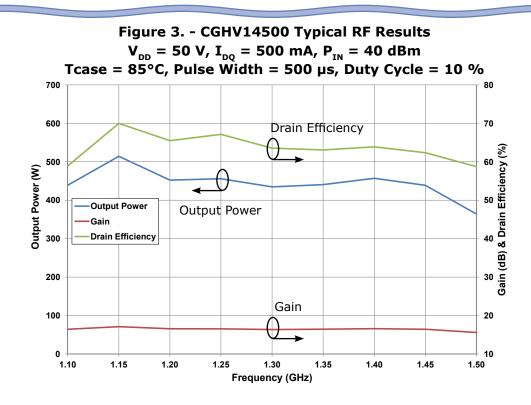
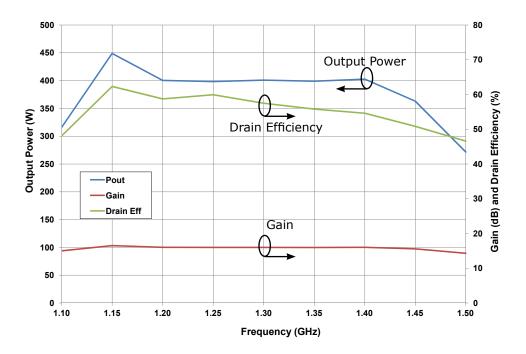


Figure 4. - CGHV14500 Typical CW RF Results $V_{DD} = 50 \text{ V}, \text{ I}_{DQ} = 500 \text{ mA}, P_{IN} = 40 \text{ dBm}, \text{ Tcase} = 50^{\circ}\text{C}$

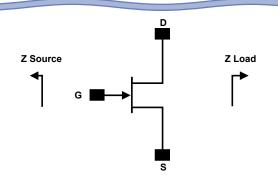


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Source and Load Impedances



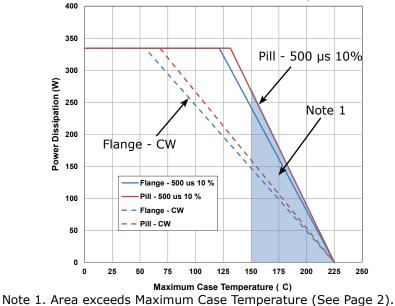
Frequency (MHz)	Z Source	Z Load
900	0.3 - j0.3	2.1 + j1.4
1000	0.3 - j0.4	2.0 +j0.7
1100	0.6 - j0.4	1.8 + j0.9
1200	0.8 - j0.7	1.5 + j0.9
1300	1.1 - j0.7	1.3 + j0.7
1400	1.2 - j0.1	1.2 + j0.5
1500	1.8 - j0.1	1.1 + j0.4

Note 1. $V_{_{DD}}$ = 50 V, $I_{_{DQ}}$ = 500 mA in the 440117 package Note 2. Optimized for power gain, $P_{_{SAT}}$ and Drain Efficiency

Note 3. When using this device at low frequency, series resistors should be used to maintain amplifier stability

CGHV14500 Power Dissipation De-rating Curve

Figure 5. - CGHV14500 Transient Power Dissipation De-Rating Curve



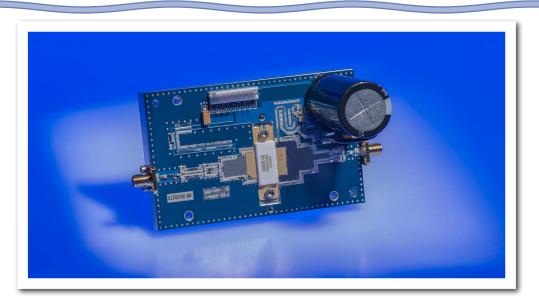
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Designator	Description	Qty
R1	RES, 1/16W, 0603, 1%, 562 OHMS	1
R2	RES, 5.1 OHM, +/-1%, 1/16W, 0603	1
R3	RES, 1/16W, 0603, 1%, 4700 OHMS	1
L1	INDUCTOR, CHIP, 6.8 nH, 0603 SMT	1
C1, C23	CAP, 27pF, +/- 5%, 250V, 0805, ATC 600F	2
C2	CAP, 2.0pF, +/- 0.1pF, 0603, ATC	1
C3, C4	CAP, 1.5pF, +/-0.05pF, 250V, 0805, ATC 600F	2
C5,C6	CAP, 1.8pF, +/-0.1pF, 250V, 0805, ATC 600F	2
C7,C8	CAP, 4.3pF, +/-0.1pF, 250V, 0805, ATC 600F	2
C9,C10	CAP, 7.5pF, +/-0.1pF, 250V, 0805, ATC 600F	2
C11,C24	CAP, 47pF,+/-5%, 250V, 0805, ATC 600F	2
C12,C25	CAP, 100pF, +/-5%, 250V, 0805, ATC 600F	2
C13,C26	CAP, 33000PF, 0805,100V, X7R	2
C14	CAP 10uF 16V TANTALUM	1
C15,C16	CAP, 5.6pF, +/-0.1pF, 250V, 0805, ATC 600F	2
C17,C18	CAP, 3.6pF, +/-0.1pF, 250V, 0805, ATC 600F	2
C19,C20	CAP, 2.0pF, +/-0.1pF, 250V, 0805, ATC 600F	2
C21,C22	CAP, 0.7pF, +/-0.05pF, 0805, ATC 600F	2
C27	CAP, 1.0UF, 100V, 10%, X7R, 1210	1
C28	CAP, 3300 UF, +/-20%, 100V, ELECTROLYTIC	1
J1,J2	CONN, SMA, PANEL MOUNT JACK, FL	2
J3	HEADER RT>PLZ .1CEN LK 9POS	1
J4	CONNECTOR ; SMB, Straight, JACK, SMD	1
W1	CABLE ,18 AWG, 4.2	1
	PCB, RO4350B, 0.020' MIL THK, CGHV14500, 1.2-1.4GHZ	1
Q1	CGHV14500	1

CGHV14500-TB Demonstration Amplifier Circuit

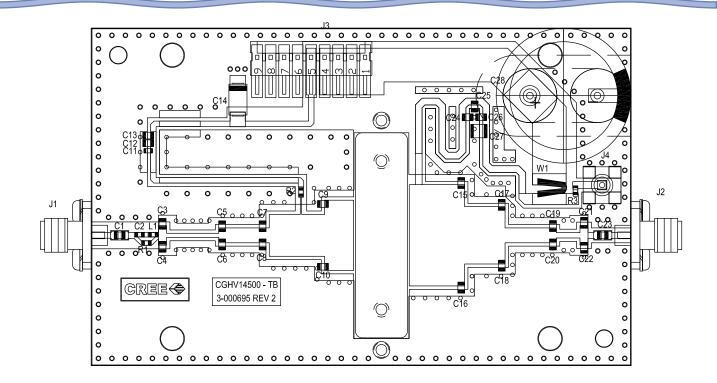


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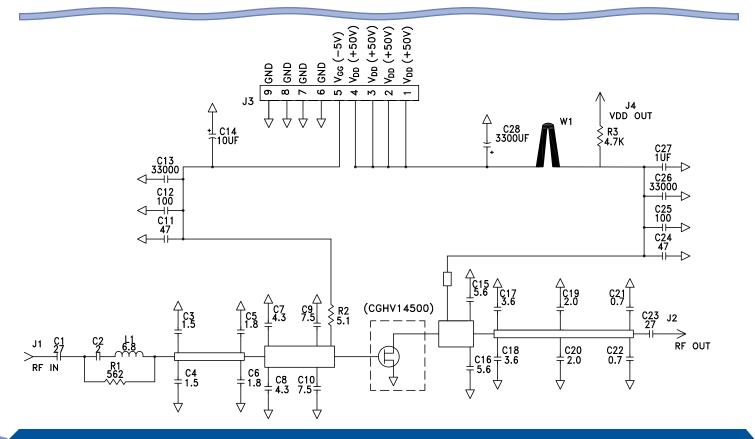
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CGHV14500-TB Demonstration Amplifier Circuit Outline



CGHV14500-TB Demonstration Amplifier Circuit Schematic

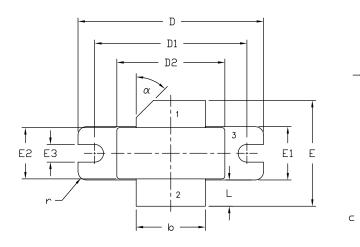


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Product Dimensions CGHV14500F (Package Type – 440117)



NOTES:

.002

A1

PIN 1. GATE 2. DRAIN

Δ

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M -1994.

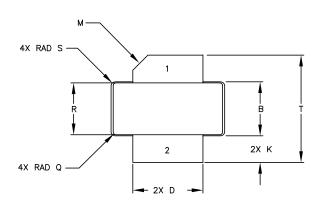
2. CONTROLLING DIMENSION: INCH.

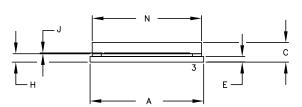
3. ADHESIVE FROM LID MAY EXTEND A MAXIMUM OF 0.020' BEYOND EDGE OF LID.

4. LID MAY BE MISALIGNED TO THE BODY OF PACKAGE BY A MAXIMUM OF 0.008" IN ANY DIRECTION.

AI		INC	HES	MILLIMETERS		NOTES
	DIM	MIN	MAX	MIN	MAX	
	А	0.138	0.158	3.51	4.01	
	A1	0.057	0.067	1.45	1.70	
_	A2	0.035	0.045	0.89	1.14	
	b	0.495	0.505	12.57	12.83	2×
	с	0.003	0.006	0.08	0.15	
	D	1.335	1.345	33.91	34.16	
	D1	1.095	1.105	27.81	28.07	
_	D2	0.773	0.787	19.63	20.00	
- A2	E	0.745	0.785	18.92	19.94	
	E1	0.380	0.390	9.65	9.91	
	E2	0.365	0.375	9.72	9.53	
	E3	0.123	0.133	3.12	3.38	
1. GATE	L	0.170	0.210	4.32	5.33	2x
2. DRAIN	r	0.06	TYP	0.06	TYP	4x
3. SOURCE	α	45 '	REF	45 '	REF	

Product Dimensions CGHV14500P (Package Type - 440133)





NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.
- 3. ADHESIVE FROM LID MAY EXTEND A MAXIMUM OF 0.020" BEYOND EDGE OF LID.
- 4. LID MAY BE MISALIGNED TO THE BODY OF PACKAGE BY A MAXIMUM OF 0.008" IN ANY DIRECTION.

1 1 1					
	INC	HES	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
А	0.805	0.815	20.45	20.70	
В	0.380	0.390	9.65	9.91	
С	0.135	0.149	3.43	3.78	
D	0.495	0.505	12.57	12.83	
Е	0.035	0.045	.89	1.14	
н	0.057	0.067	1.45	1.70	
J	0.003	0.006	.08	.15	
к	0.170	0.210	4.32	5.33	
М	45 '	REF	45' REF		
N	0.773	0.787	19.63	19.99	
Q	0.020 REF		0.51	REF	
R	0.364	0.374	9.25	9.50	
S	0.030 REF		0.76	REF	
T	0.745	0.785	18.92	19.94	

STYLE 1:

PIN 1. GATE

2. DRAIN

3. SOURCE

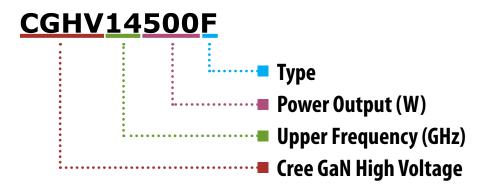
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Part Number System



Parameter	Value	Units	
Upper Frequency ¹	1.4	GHz	
Power Output	500	W	
Туре	F = Flanged P = Package	-	



Note¹: Alpha characters used in frequency code indicate a value greater than 9.9 GHz. See Table 2 for value.

Character Code	Code Value
А	0
В	1
С	2
D	3
E	4
F	5
G	6
Н	7
J	8
К	9
Examples:	1A = 10.0 GHz 2H = 27.0 GHz

Table 2.

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