



General Description

The AOZ1401 is an integrated Li+ charger protection IC that protects against input over-voltage, input overcurrent, and battery over-voltage conditions. When the device detects an input over-voltage condition, it disconnects output from input by turning off the internal MOSFET switch, preventing damage of the charger. When the device detects a battery over-voltage condition, it also turns off the internal MOSFET to prevent battery from being over-charged. In the case of an overcurrent condition, it limits the input current at the current limit level set by an external resistor, and if the overcurrent persists, it turns off the internal MOSFET after a blanking period. The thermal shutdown feature adds another layer of protection.

The fault status indicator provides status information about fault conditions to the host.

The AOZ1401 is available in a 2mm x 2mm 8-pin DFN package and is rated over a -40°C to +85°C ambient temperature range.

Features

- Provides protection for three variables:
 - Input over-voltage
 - User-programmable input over-current
 - Battery over-voltage
- 30V maximum input voltage
- Supports up to 1.5A input current
- High immunity against false triggering due to voltage spikes
- Robust against false triggering due to current transients
- Thermal Shutdown
- Status indication, fault condition
- Available in space-saving small 8 lead 2x2 DFN

Applications

- Smart Phones
- PDAs
- MP3 Players
- Low-Power handheld devices
- Bluetooth headsets



VDC VIN C1 OUT 8 AC Adaptor 1µF C2 1μF Charger R1 $47 k\Omega$ End AOZ1401 $\Lambda \Lambda \Lambda$ R4 System 100kΩ R2 VBAT 6 47kΩ FAULTB \sim ENB 5 \sim R3 ILIM GND $47 k\Omega$ 7 2 R5 $25k\Omega$

Typical Application



Ordering Information

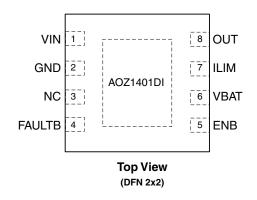
Part Number	Ambient Temperature Range	Package	Environmental		
AOZ1401DI	-40°C to +85°C	2 x 2 DFN-8	RoHS Compliant		



All AOS products are offered in packages with Pb-free plating and compliant to RoHS standards.

Parts marked as Green Products (with "L" suffix) use reduced levels of Halogens, and are also RoHS compliant. Please visit www.aosmd.com/web/quality/rohs_compliant.jsp for additional information.

Pin Configuration



Pin Description

Pin Name	Pin Number	I/O	Description		
VIN	1	I	Input power, connect to external DC supply. Connect external 1µF (minimum) to GND		
GND	2		Ground terminal		
NC	3		These pins may have internal circuits used for test purposes. Do not make any external connections at these pins for normal operation.		
FAULTB 4 O		0	Open-drain output, device status. FAULTB = LO indicates that the input FET has been turned off due to input over-voltage or input over-current conditions, or because the battery voltage is outside safe limits.		
ENB	5	I	Chip enable input. Active low. When ENB = HI, the input FET is off. Internally pulled down.		
VBAT	6	I	Battery voltage sense input. Connect to pack positive terminal through a resistor.		
ILIM	7	I/O	Input over-current threshold programming. Connect a resistor to GND to set the over-current threshold.		
OUT	8	0	Output terminal to the charging system. Connect external 1µF capacitor (minimum) to GND		
Therm	Thermal PAD		There is an internal electrical connection between the exposed thermal pad and the GND pin of the device. The thermal pad must be connected to the same potential as the GND pin on the printed circuit board. Do not use the thermal pad as the primary ground input for the device. GND pin must be connected to ground at all times.		

Absolute Maximum Ratings⁽¹⁾

Parameter	Pin Name	Rating		
Input Voltage	VIN (with respect to GND)	-0.3V to 30V		
Input Voltage	OUT(with respect to GND)	-0.3V to 12V		
Input Voltage	ILIM, FAULTB, ENB, VBAT (with respect to GND)	–0.3V to 7V		
Input Current	VIN	2.0A		
Output Current	OUT	2.0A		
Output Sink Current	FAULTB	15mA		
Junction Temperature, T _J		–40°C to 150°C		
Storage Temperature, T _{STG}		–65°C to 150°C		
Lead Temperature (Soldering, 10 seconds)		300°C		
ESD (Human Body Model)		2kV		
ESD (Machine Model)		200V		

Note:

1. Stresses beyond those listed under absolute maximum ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under recommended operating conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. All voltage values are with respect to the network ground terminal unless otherwise noted.

Recommended Operating Conditions

Parameter	Conditions	Min.	Max.	Units
V _{VIN}	VIN Voltage Range	3.3	26	V
I _{VIN}	Input Current, VIN pin		1.5	A
lout	Current, OUT pin		1.5	A
R _{ILIM}	OCP Programming Resistor	15	90	kΩ
TJ	Junction Temperature	0	125	°C

Electrical Characteristics

 $T_A = 25^{\circ}C$ and VIN = 5V, unless otherwise stated. Specifications in **BOLD** are guaranteed by design for ambient temperature range of -40°C to +85°C

Symbol Parameter		Test Conditions	Min.	Тур.	Max.	Units
VIN		I				
V _{UVLO}	Under-Voltage Lock-out, Input Power Detected Threshold	ENB = LO	2.6	2.7	2.8	V
V _{HYS-UVLO}	Hysteresis on UVLO	ENB = LO		260		mV
T _{DGL(PGOOD)}	Deglitch Time, Input Power Detected Status	ENB = LO		8		ms
I _{DD}	Operating Current	ENB = LO, No Load on OUT pin, VIN = 5.0V, $R_{ILIM} = 25k\Omega$		400	500	μA
I _{STDBY}	Standby Current	ENB = HI, R_{ILIM} = 25k Ω		70	95	μA
	TPUT CHARACTERISTICS	•				
V _{DO}	Drop-Out Voltage VIN to OUT	ENB = LO, I _{OUT} = 1A			300	mV
INPUT OVER-	VOLTAGE PROTECTION		•			
V _{OVP}	Input Over-Voltage Protection Threshold	ENB = LO	5.67	5.85	6.00	V
t _{PD(OVP)}	Input OV Propagation Delay	ENB = LO			1	μs
V _{HYS-OVP}	Hysteresis on OVP	ENB = LO or HI		60		mV
t _{ON(OVP)}	Recovery Time from Input Over-voltage Condition	ENB = LO		8		ms
INPUT OVER-	CURRENT PROTECTION		•			
	Input Over-Current Protection Threshold Range	ENB = LO	300		1500	mA
I _{OCP}	Input Over-Current Protection Threshold	ENB = LO, R_{ILIM} = 25k Ω	930	1000	1070	mA
K _{ILIM}	Current Limit Programming: I _{OCP} = K _{ILIM} / R _{ILIM}			25		kΩ
t _{BLANK(OCP)}	Blanking Time, Input Over-Current Detected	ENB = LO		176		μs
t _{ON(OCP)}	Recovery Time from Input Over-current Condition	ENB = LO		64		ms
BATTERY OV	ER-RANGE PROTECTION		•			
BV _{OVP}	Battery Over-Voltage Protection Threshold	ENB = LO, VIN > 4.4V	4.30	4.35	4.4	V
V _{HYS-BOVP}	Hysteresis on BV _{OVP}	ENB = LO, VIN > 4.4V		270		mV
I _{VBAT}	Input Bias Current on VBAT pin				20	nA
T _{DGL(BOVP)}	Deglitch Time, Battery Over-Voltage Detected	ENB = LO		176		μs
THERMAL PR	OTECTION		1			
T _{J(OFF)}	Thermal Shutdown Temperature			140		°C

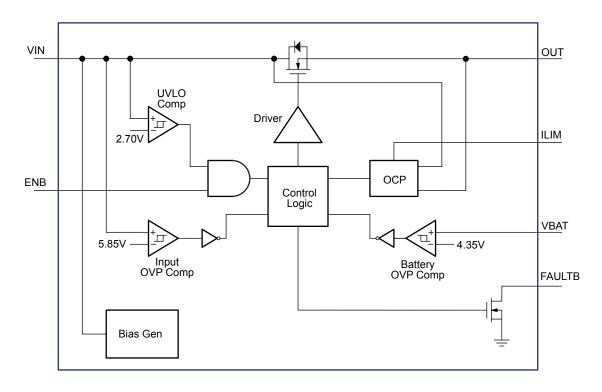


Electrical Characteristics (Continued)

 $T_A = 25^{\circ}C$ and VIN = 5V, unless otherwise stated. Specifications in **BOLD** are guaranteed by design for ambient temperature range of -40°C to +85°C

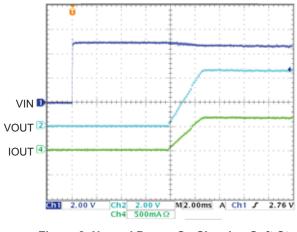
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units				
T _{J(OFF-HYS)}	Thermal Shutdown Hysteresis			20		°C				
LOGIC LEVELS ON ENB										
V _{IL}	Logic LOW Input Voltage		0		0.4	V				
V _{IH}	Logic HIGH Input Voltage		1.4			V				
IIL		ENB = 0V			1	μA				
I _{IH}		ENB = 1.8V			15	μA				
LOGIC LEVEL	LOGIC LEVELS ON FAULTB									
V _{OL}	Output LOW Voltage	I _{SINK} = 5mA			0.2	V				
I _{HI-Z}	Leakage Current	V _{FAULTB} = 5V			10	μA				

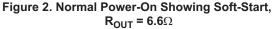
Functional Block Diagram





Functional Characteristics





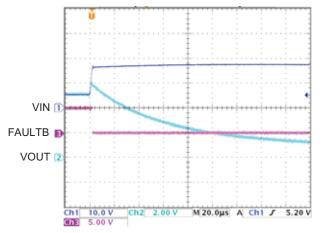
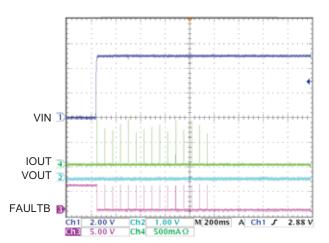
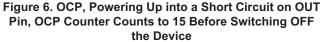


Figure 4. OVP Response for Input Step, V_{IN} = 5V to 12V,





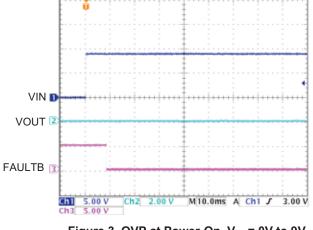


Figure 3. OVP at Power-On, V_{IN} = 0V to 9V

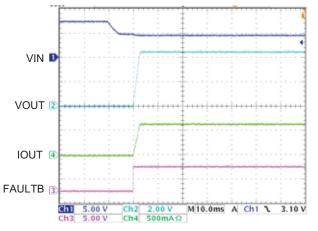
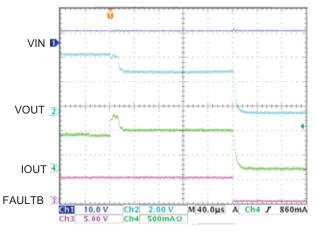


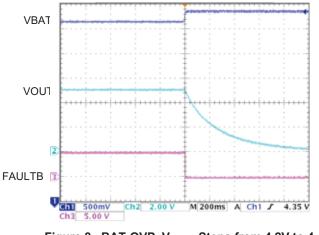
Figure 5. Recovery from OVP, V_{IN} = 7.5V to 5V







Functional Characteristics (Continued)



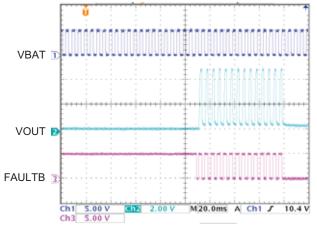


Figure 9. Battery OVP and Counter

2.80 2.75 2.70 2.65 UVLO (V) Increasing 2.60 Decreasing 2.55 2.50 2.45 2.40 -50 -30 -10 10 30 50 70 90 110 130 Temp (°C) Figure 10. UVLO vs. Temperature

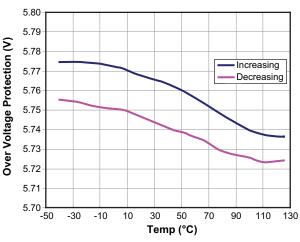


Figure 12. Over-voltage Protection vs. Temperature

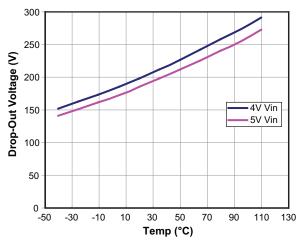


Figure 11. Drop-out Voltage vs. Temperature

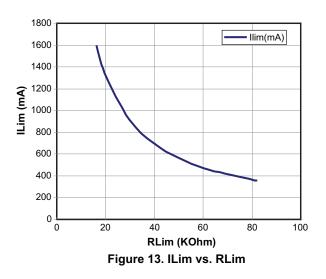


Figure 8. BAT-OVP, V_{VBAT} Steps from 4.2V to 4.4V

Typical Operating Characteristics



Typical Operating Characteristics (Continued)

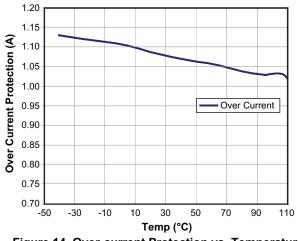


Figure 14. Over-current Protection vs. Temperature

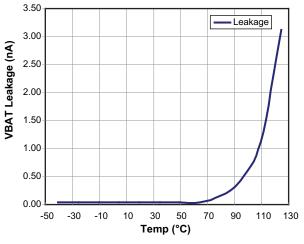


Figure 16. VBAT Leakage Current vs. Temperature

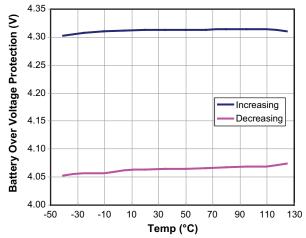


Figure 15. Battery Over Voltage Protection vs. Temperature

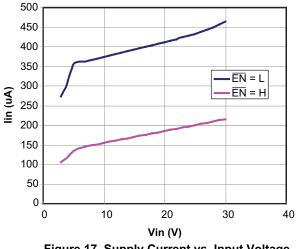
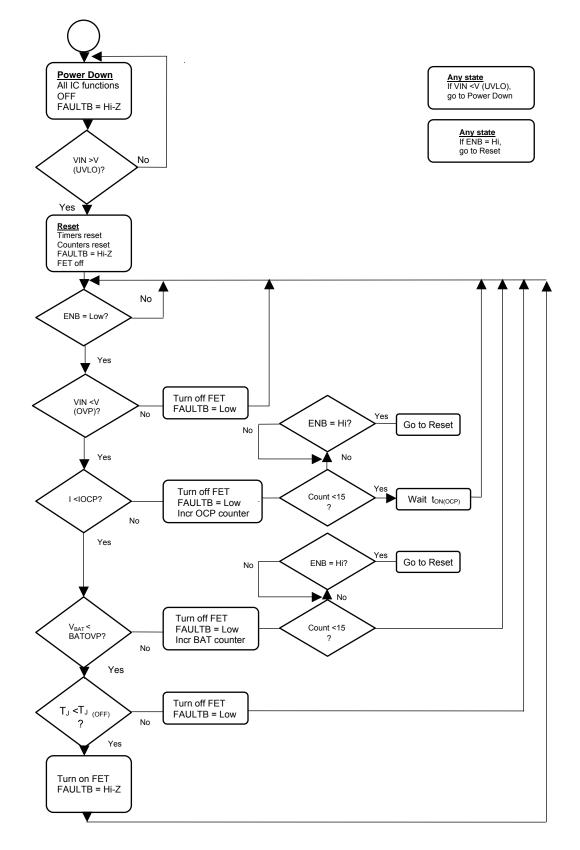


Figure 17. Supply Current vs. Input Voltage



Flow Chart





Theory of Operation

The AOZ1401DI is an integrated Li+ charger protection IC that protects against input over-voltage, input overcurrent, and battery over-voltage conditions.

When the device detects an input over-voltage condition, it turns off the internal MOSFET, preventing damage of the charger.

When the device detects a battery over-voltage condition, it also turns off the internal MOSFET to prevent battery from being over-charged.

In the case of an over-current condition, it limits the input current at the current limit level set by an external resistor, and if the over-current persists, it turns off the internal MOSFET after a blanking period.

The thermal shutdown feature adds another layer of protection.

The fault status indicator provides status information about fault conditions to the host.

Under-Voltage Lockout (UVLO)

The under-voltage lockout (UVLO) circuit of AOZ1401DI monitors the input voltage and prevents the output MOSFET from turning on until V_{IN} exceeds 2.7V.

Enable

The ENB pin is the ON/OFF control for the output switch. When the ENB pin is driven high, the internal FET is turned off. When the ENB pin is low, the FET is turned on.

Input Overvoltage Protection

The device continuously monitors the input voltage, the input current, and the battery voltage. If the input voltage rises above V_{OVP}, the internal FET is turned off, removing power from the circuit. When the input voltage returns below V_{OVP}, but above V_{UVLO}, the FET is turned on again after a deglitch time of t_{ON (OVP)} to ensure that the input supply has stabilized.

Input Overcurrent Protection

In the case of an over-current condition, AOZ1401DI limits the input current at the current limit level set by an external resistor, and if the over-current persists, it turns off the internal MOSFET after a blanking period. The overcurrent threshold is programmed by a resistor $R_5 (R_{ILIM})$ connected from the ILIM pin to VSS. Figure 13 shows the OCP threshold as a function of $R_5 (R_{ILIM})$, and may be approximated by the following equation:

 I_{OCP} = 25 ÷ R₅ (R_{ILIM}) (current in A, resistance in k Ω).

Battery Over-voltage Protection

The battery overvoltage threshold BVOVP is internally set to 4.35V. If the battery voltage exceeds the BVOVP threshold, the FET is turned off, and the FAULTB pin is driven low.

FAULTB

The FAULTB pin is an open drain output that is asserted low when either an input overvoltage, input overcurrent battery overvoltage, or thermal overload condition occurs.

Thermal Shut-down Protection

The thermal overload protection of AOZ1401DI is engaged to protect the device from damage should the die temperature exceeds safe margins due to a short circuit, extreme loading or heating from external sources.

Application Information

Input Capacitor Selection

Use a 1μ F or larger capacitor for input bypassing. This will limit the input voltage drop during output transient conditions. 1μ F capacitor should be adequate for most applications; however, higher capacitor values will further reduce the voltage drop. Place the bypass capacitor as close to the IN pins as feasibly possible.

Output Capacitor Selection

Use a 1μ F or larger capacitor between the OUT and GND pins. The capacitance does not affect the turn on slew rate; however, a larger capacitor will make the initial turn on transient smoother.

Selection of R₄ (R_{VBAT})

Direct-connecting battery to VBAT is not recommended. The voltage at the IN pin may appear on the VBAT pin if IC fails. Connecting the VBAT pin through R_4 (R_{VBAT}) prevents a large current from flowing into the battery in case of a failure of the IC, so choose R_4 (R_{VBAT}) value in the range 100k Ω to 470k Ω .

Selection of R₃ (R_{EN}), R₂ (R_{FAULTB}), and R₁ (R_{PU})

The ENB pin can be used to enable and disable the IC. ENB pin can be tied to ground or left un-connected, permanently enabling the device, if host is not presented. If host control is required, the ENB pin should be connected to the host through as large a resistor as possible.

The FAULTB pin is an open drain output that is asserted low when either an input overvoltage, input overcurrent battery overvoltage, or thermal overload condition occurs. If the application does not require monitoring of



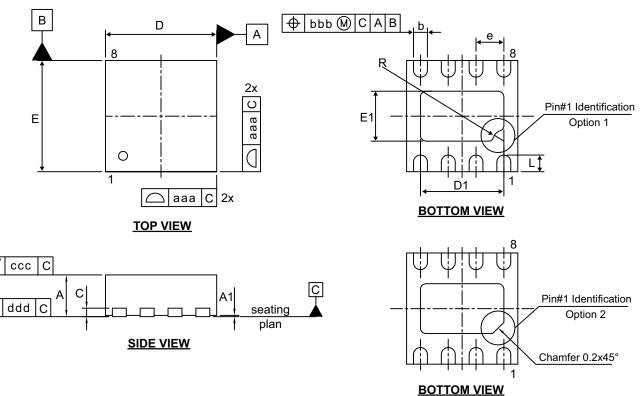
the FAULTB pin, it can be left unconnected. But if the FAULTB pin has to be monitored, it should be pulled high externally through R₁ (R_{PU}), and connected to the host through R₂ (R_{FAULTB}) prevents damage to the host controller if the AOZ1401DI fails. Choose the resistors with high values, and values between $22k\Omega$ and $100k\Omega$ should be sufficient.

PCB Layout Guidelines

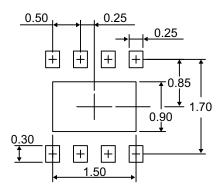
AOZ1401DI uses DFN 2x2 package with a thermal PAD. The thermal PAD should be thermally coupled with the PCB ground plane for good thermal performance, This will require a copper pad directly under the IC in most applications. This copper pad should be connected to the ground plane with an array of thermal vias. C_{IN} , C_{OUT} , R_{ILIM} and R_{BAT} should be located close to the IC.



Package Dimensions, DFN 2x2-8L



RECOMMENDED LAND PATTERN



UNIT: mm

Dimensi	ons in	millim	neters		Dimer	nsions	in incl	hes
Symbols	Symbols Min. Nom. Max.		Symbols	Min.	Nom.	Max.		
Α	0.70	0.75	0.80		А	0.028	0.030	0.031
A1	0.00	0.02	0.05		A1	0.000	0.001	0.002
b	0.18	0.25	0.30		b	0.007	0.010	0.012
С	0.20 REF				С	0	.008 RE	F
D	1.90	2.00	2.10		D	0.075	0.079	0.083
D1	1.35	1.50	1.60		D1	0.053	0.059	0.063
E	1.90	2.00	2.10		Е	0.075	0.079	0.083
E1	0.75	0.90	1.00		E1	0.030	0.035	0.039
е	(0.50 BSC)		е	0.020 BSC		
L	0.20	0.30	0.40		L	0.008	0.012	0.016
R		0.20			R		0.008	
aaa		0.15			aaa		0.006	
bbb		0.10			bbb		0.004	
ccc		0.10			ссс		0.004	
ddd		0.08			ddd		0.003	

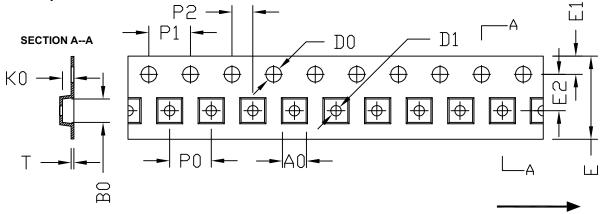
Notes:

- 1. Dimensioning and tolerancing conform to ASME Y14.5M-1994.
- 2. Controlling dimension is in millimeter, converted inch dimensions are not necessarily exact.
- 3. Dimension **b** applies to matellized terminal and is measured between 0.10mm and 0.30mm from the terminal tip. If the terminal has the optional radius on the other end of the terminal, the dimension **b** should not be measured in that radius area.
- 4. Coplanarity **ddd** applies to the terminals and all other bottom surface metallization.



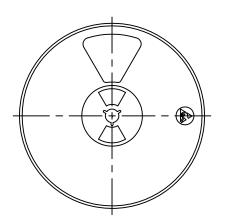
Tape and Reel Dimensions, DFN 2x2-8L

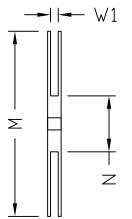
Carrier Tape

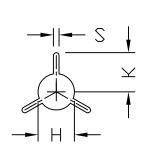


UNIT: MM	UNIT: MM FEEDING DIRECTION												
Package	A0	В0	K0	D0	D1	Е	E1	E2	P0	P1	P2	т	
	2.25	2.25	1.00	1.50	1.00	8.00	1.75	3.50	4.00	4.00	2.00	0.254	
DFN 2x2	±0.05	±0.05	±0.05	±0.10	±0.25	±0.30	±0.10	±0.05	±0.10	±0.10	±0.05	±0.02	
						-0.10							

Reel



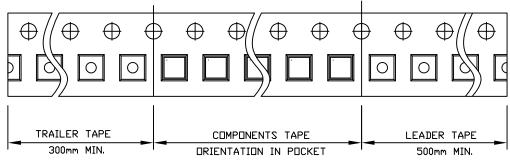




FEEDING DIRECTION

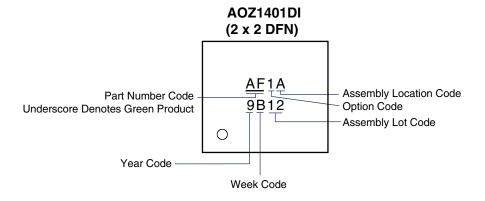
UNIT: MM	UNIT: MM											
Tape Size	Reel Size	м	Ν	W1	W2	Н	S	к				
8mm	Ø177.8	Ø177.8 MAX.	53.6 MIN.	8.4 +2.5 -0.0	14.4 MAX.	13.0 +0.5 -0.3	1.5 MIN.	10.1 MIN.				

Leader/Trailer and Orientation





Part Marking



This data sheet contains preliminary data; supplementary data may be published at a later date. Alpha & Omega Semiconductor reserves the right to make changes at any time without notice.

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As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user. 2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.