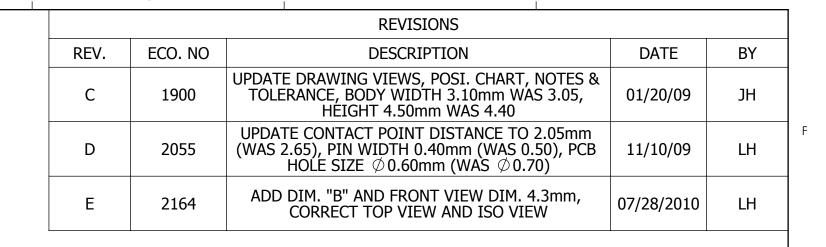
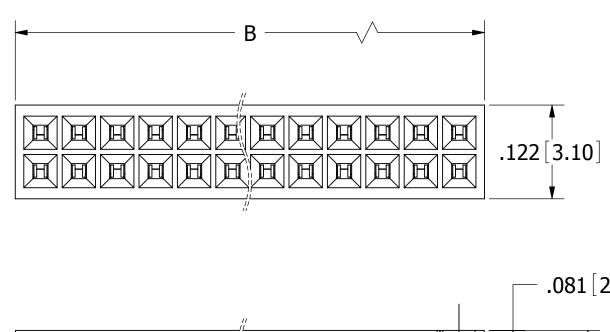
POSITIONS/		A .	F	3	
	INCH±.008		INCH±.012 MM±0.30		
02/04	0.050	1.27	0.116	2.94	
03/06	0.100	2.54	0.166	4.21	
04/08	0.150	3.81	0.216	5.48	
05/10	0.200	5.08	0.266	6.75	
06/12	0.250	6.35	0.316	8.02	
07/14	0.300	7.62	0.366	9.29	
08/16	0.350	8.89	0.416	10.56	
09/18	0.400	10.16	0.466	11.83	
10/20	0.450	11.43	0.516	13.10	
11/21	0.500	12.70	0.566	14.37	
12/24	0.550	13.97	0.616	15.64	
13/26	0.600	15.24	0.666	16.91	
14/28	0.650	16.51	0.716	18.18	
15/30	0.700	17.78	0.766	19.45	
16/32	0.750	19.05	0.816	20.72	
17/34	0.800	20.32	0.866	21.99	
18/36	0.850	21.59	0.916	23.26	
19/38	0.900	22.86	0.966	24.53	
20/40	0.950	24.13	1.016	25.80	
21/42	1.000	25.40	1.066	27.07	
22/44	1.050	26.67	1.116	28.34	
23/46	1.100	27.94	1.166	29.61	
24/48	1.150	29.21	1.216	30.88	
25/50	1.200	30.48	1.266	32.15	
26/52	1.250	31.75	1.316	33.42	
27/54	1.300	33.02	1.366	34.69	
28/56	1.350	34.29	1.416	35.96	
29/58	1.400	35.56	1.466	37.23	
30/60	1.450	36.83	1.516	38.50	
31/62	1.500	38.10	1.566	39.77	
32/64	1.550	39.37	1.616	41.04	
33/66	1.600	40.64	1.666	42.31	
34/68	1.650	41.91	1.716	43.58	
35/70	1.700	43.18	1.766	44.85	
36/72	1.750	44.45	1.816	46.12	
37/74	1.800	45.72	1.866	47.39	
38/76	1.850	46.99	1.916	48.66	
39/78	1.900	48.26	1.966	49.93	
40/80	1.950	49.53	2.016	51.20	
41/82	2.000	50.80	2.066	52.47	
42/84	2.050	52.07	2.116	53.74	
43/86	2.100	53.34	2.166	55.01	
44/88	2.150	54.61	2.216	56.28	
45/90	2.200	55.88	2.266	57.55	
46/92	2.250	57.15	2.316	58.82	
47/94	2.300	58.42	2.366	60.09	
48/96	2.350	59.69	2.416	61.36	
49/98	2.400	60.96	2.466	62.63	
50/100	2.450	62.23	2.516	63.90	
= -1 =				•	

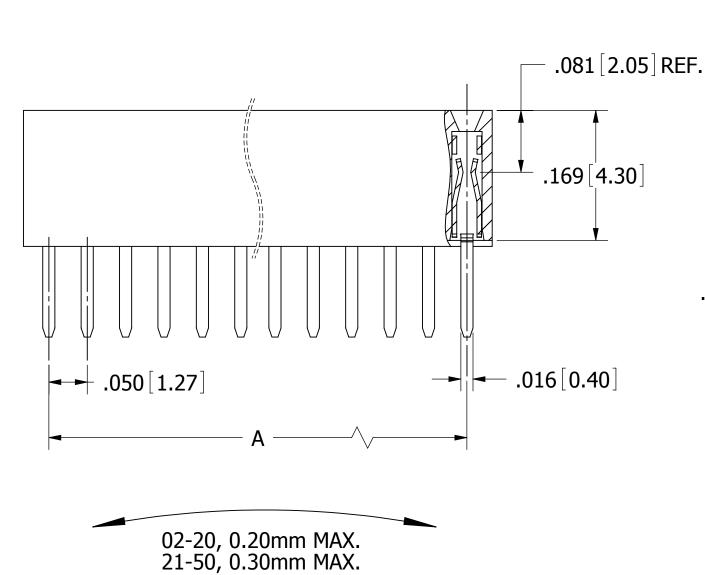
PART NUMBER CODING LPPB_ _2CFFN-RC

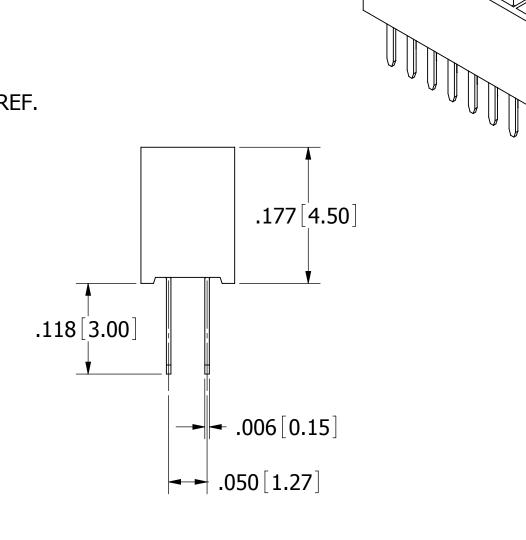
NUMBER OF POSITIONS

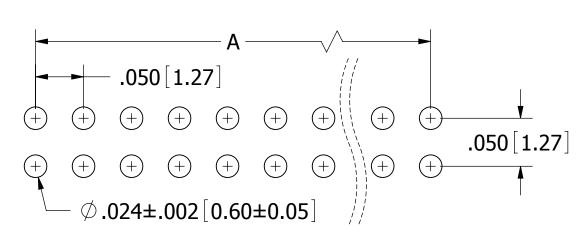
(CONTACTS PER ROW, 2 THRU 50)











RECOMMENDED PCB HOLE LAYOUT

Rohs Compliant

UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCHES[MM]
TOLERANCES:
ANGULAR:
.XX=±.01 [.3]
.XXX=±.008 [.20]
.XXXX=±.0040 [.100]
SURFACE FINISH: 63 Ra
REMOVE ALL BURRS AND SHARP EDGES .010 MAX

INTERPRET DIMENSIONS AND GEOMETRIC TOLERANCING PER: ANSI Y14.5M-1994

	DATE	NAME					_
DRAWN	1/20/09	JH			JLLI	NS	5
THE INFORMATION HEREIN CONTAINS PROPIETARY INFORMATION OF		CONNECTOR SOLUTIONS					
SULLINS ELECTRONICS AND IS NOT TO BE REPRODUCED, USED OR DISCLOSED TO OTHERS FOR ANY PURPOSE EXCEPT AS SPECIFICALLY AUTHORIZED IN WRITING BY AN OFFICER OF SULLINS ELECTRONICS.			DESCRIPTION				
			PART NUMBER LPPB2CFFN-RC				
+			SIZE	DWG. NO.			REV
(\bigcirc)			C	10	956		Ε
\Box			SCALE	: 12:1		SHEET	1 OF 1

NOTES:

- INSULATOR MATERIAL: L.C.P.
- *PROCESSING TEMPERATURE = 260° C FOR 10 SEC.
- CONTACT MATERIAL: PHOSPHOR BRONZE.
- PLATING: GOLD FLASH OVERALL.
- OPERATING TEMPERATURE: -40°C TO +105° C.
- INSULATION RESISTANCE: 5000 MEGA OHMS MIN.
- CONTACT RESISTANCE: 20 MILLI OHMS MAX.
- DIELECTRIC WITHSTANDING: AC 500V.
- UL FLAMMABILITY RATING: 94V-0.
- 9. CURRENT RATING: 1 AMP.
 - *INDICATED TEMPERATURE AND TIME IS FOR COMPONENT INSULATOR. HIGHER PROCESSING TEMPERATURES MAY BE USED, PROVIDED HEAT IS APPLIED FROM BACK SIDE OF PCB, AND INSULATOR DOES NOT EXCEED INDICATED TEMPERATURE AND TIME.