

Vishay Siliconix

P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (mA)			
- 20	8 at V _{GS} = - 4.5 V	- 150			
	12 at V _{GS} = - 2.5 V	- 125			
	15 at V _{GS} = - 1.8 V	- 100			
	20 at V _{GS} = - 1.5 V	- 30			



Ordering Information: Si1033X-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET: 1.5 V Rated
- High-Side Switching
- Low On-Resistance: 8 Ω
- Low Threshold: 0.9 V (typ.)
- Fast Switching Speed: 45 ns (typ.)
- 1.5 V Operation
- Gate-Source ESD Protected: 2000 V
- Compliant to RoHS Directive 2002/95/EC

BENEFITS

- Ease in Driving Switches
- Low Offset (Error) Voltage
- Low-Voltage Operation
- High-Speed Circuits
- Low Battery Voltage Operation

APPLICATIONS

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories
- Battery Operated Systems
- Power Supply Converter Circuits
- Load/Power Switching Cell Phones, Pagers

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)						
Parameter	Symbol	5 s	Steady State	Unit		
Drain-Source Voltage		V _{DS}	- 20		V	
Gate-Source Voltage		V _{GS}	± 5			
Continuous Drain Current (T $= 150 ^{\circ}\text{C})^{a}$	T _A = 25 °C	- I _D	- 155	- 145		
Continuous Drain Current $(T_j = 150 \text{ C})$	T _A = 85 °C		- 110	- 105		
Pulsed Drain Current ^b		I _{DM}	- 650		ma	
Continuous Source Current (Diode Conduction) ^a	۱ _S	- 450	- 380			
Mariana Devez Dissis di sal	T _A = 25 °C	P _D	280	250	mW	
Maximum Power Dissipation~	T _A = 85 °C		145	130		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	
Gate-Source ESD Rating (HBM, Method 3015)		ESD	2000		V	

Notes:

a. Surface mounted on FR4 board.

b. Pulse width limited by maximum junction temperature.



FREE

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SPECIFICATIONS (T _A = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 0.40		- 1.20	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 2.8 V$		± 0.5	± 1		
		$V_{DS} = 0 V, V_{GS} = \pm 4.5 V$ ± 1				μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 16 V, V _{GS} = 0 V		- 1	- 500	nA	
		$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			- 10	μA	
On-State Drain Current ^a	I _{D(on)}	V_{DS} = - 5 V, V_{GS} = - 4.5 V	- 200			mA	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 150 mA			8	Ω	
		V _{GS} = - 2.5 V, I _D = - 125 mA			12		
		V _{GS} = - 1.8 V, I _D = - 100 mA			15		
		V _{GS} = - 1.5 V, I _D = - 30 mA			20		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 150 mA		0.4		S	
Diode Forward Voltage ^a	V _{SD}	I _S = - 150 mA, V _{GS} = 0 V			- 1.2	V	
Dynamic ^b							
Total Gate Charge	Qg			1500			
Gate-Source Charge	Q _{gs}	V_{DS} = - 10 V, V_{GS} = - 4.5 V, I_D = - 150 mA		150		рС	
Gate-Drain Charge	Q _{gd}			450			
Turn-On Delay Time	t _{d(on)}				55		
Rise Time	t _r	V_{DD} = - 10 V, R_L = 65 Ω			30	20	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 150 mA, V_{GEN} = - 4.5 V, R_g = 10 Ω			60	115	
Fall Time	t _f				30		

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



2.5

3.0

2.0

125 °C

25



TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)





Si1033X

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TYPICAL CHARACTERISTICS $T_A = 25 \text{ °C}$, unless otherwise noted)



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?71428.



SC89: 6- LEADS (SOT-563F)



NOTES:

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- 1. Dimensions in millimeters.
- Dimension D does not include mold flash, protrusions or gate burrs. Mold flush, protrusions or gate burrs shall not exceed 0.15 mm per dimension E1 does not include interlead flash or protrusion, interlead flash or protrusion shall not exceed 0.15 mm per side.
- Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, the bar burrs, gate burrs and interlead flash, but including any mismatch between the top and the bottom of the plastic body.
- A Datums A, B and D to be determined 0.10 mm from the lead tip.
 - Terminal numbers are shown for reference only.
 - These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.









	MILLIMETERS				Tolerances	
Dim	Min	Max	Note	Symbol	Position	
Α	0.56	0.60		aaa	0.10	
A1	0.00	0.10		bbb	0.10	
b	0.15	0.30		CCC	0.10	
С	0.10	0.18				
D	1.50	1.70	2, 3			
E	1.55	1.70				
E1	1.20 BSC		2, 3			
е	0.50 BSC					
e1	1.00 BSC					
L	0.35 BSC					
L1	0.20 BSC					
ECN: E-00499—Rev. B, 02-Jul-01 DWG: 5880						



Application Note 826

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RECOMMENDED MINIMUM PADS FOR SC-89: 6-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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