

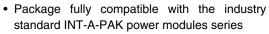
Vishay High Power Products

Three Phase Bridge (Power Modules), 60/70 A



PRODUCT SUMMARY	1
lo	60/70 A

FEATURES





· High thermal conductivity package, electrically insulated case

- · Excellent power volume ratio, outline for easy connections to power transistor and IGBT modules
- 4000 V_{RMS} isolating voltage
- UL E78996 approved
- Totally lead (Pb)-free
- Designed and qualified for industrial level

DESCRIPTION

A range of extremely compact, encapsulated three phase bridge rectifiers offering efficient and reliable operation. They are intended for use in general purpose and heavy duty applications.

SYMBOL	CHARACTERISTICS	60MT.K	70MT.K	UNITS	
1		60 (75)	70 (90)	Α	
lo	T _C	85 (61)	85 (57)	°C	
	50 Hz	420	480	А	
I _{FSM}	60 Hz	440	500		
I ² t	50 Hz	870	1150	kA ² s	
1-1	60 Hz	790	1050		
I ² √t		8700	11 500	kA²√s	
V _{RRM}	Range	800 to 1600		V	
T _{Stg}	Range	- 40 to	°C		

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS						
TYPE NUMBER	VOLTAGE CODE	PEAK REVERSE VOLTAGE PEAK REVERSE VOLTAGE		I _{RRM} MAXIMUM AT T _J MAXIMUM mA		
	80	800	900			
60-70MTK	100	1000	1100			
	120	1200	1300	10		
	140	1400	1500			
	160	1600	1700			

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60-70MT..KPbF Series

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FORWARD CONDUCTION							
PARAMETER	SYMBOL	TEST CONDITIONS			60MT.K	70MT.K	UNITS
Maximum DC output		120° rect. conduction angle		60 (75)	70 (90)	Α	
current at case temperature	I _O			85 (61)	85 (57)	°C	
	I _{FSM}	t = 10 ms	No voltage	Initial T _J = T _J maximum	420	480	Α
Maximum peak, one-cycle forward, non-repetitive		t = 8.3 ms	reapplied		440	500	
surge current		t = 10 ms	100 % V _{RRM}		350	400	
g		t = 8.3 ms	reapplied		370	420	
Maximum I ² t for fusing		t = 10 ms	No voltage		870	1150	- kA ² s
	l ² t	t = 8.3 ms	reapplied		790	1050	
		t = 10 ms	100 % V _{RRM}		610	800	
		t = 8.3 ms	reapplied		560	730	
Maximum $I^2\sqrt{t}$ for fusing	I²√t	t = 0.1 to 10 ms, no voltage reapplied		8700	11 300	A²√s	
Low level value of threshold voltage	V _{F(TO)1}	(16.7 % x π x I _{F(AV)} < I < π × I _{F(AV)}), T _J maximum 0		0.85	0.86	V	
High level value of threshold voltage	V _{F(TO)2}	$(I > \pi \times I_{F(AV)}), T_J$ maximum 1.07		1.08	V		
Low level value of forward slope resistance	r _{f1}	(16.7 % x π x $I_{F(AV)} < I < \pi \times I_{F(AV)}$), T_J maximum		8.04	7.35		
High level value of forward slope resistance	r _{f2}	$(I > \pi \times I_{F(AV)}), T_J \text{ maximum}$ 7.08 6.53		6.53	mΩ		
Maximum forward voltage drop	V_{FM}	I _{pk} = 100 A, T _J = 25 °C, t _p = 400 μs single junction 1.75 1.55		1.55			
RMS isolation voltage	V _{ISOL}	$T_J = 25$ °C, all terminal shorted f = 50 Hz, t = 1 s			>		

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	60MT.K	70MT.K	UNITS	
Maximum junction operating and storage temperature range		T _J , T _{Stg}	T _J , T _{Stg}		- 40 to 150		
			DC operation per module	0.37	0.29		
Maximum thermal resistance,		R _{thJC}		DC operation per junction	2.22	1.75	
junction to case			120° rect. conduction angle per module	0.40	0.34	K/W	
			120° rect. conduction angle per junction	2.42	2.01		
Maximum thermal resistance, case to heatsink per module		R _{thCS}	Mounting surface smooth, flat and greased	0.03			
Mounting torque ± 10 % to heatsink to terminal			A mounting compound is recommended and	4 to 6		Nm	
			the torque should be rechecked after a period		3 to 4		
Approximate weight			of 3 hours to allow for the spread of the compound. Lubricated threads.		76	g	

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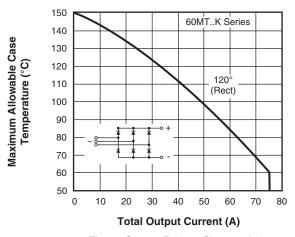


Fig. 1 - Current Ratings Characteristics

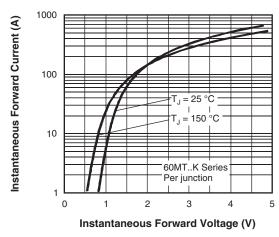


Fig. 2 - Forward Voltage Drop Characteristics

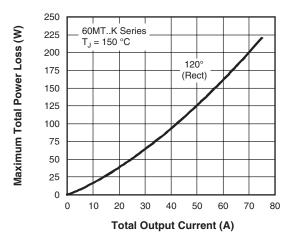
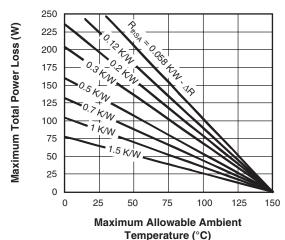


Fig. 3 - Total Power Loss Characteristics



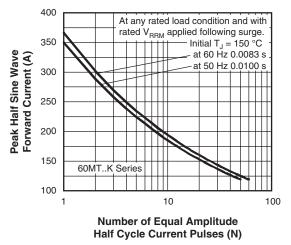


Fig. 4 - Maximum Non-Repetitve Surge Current

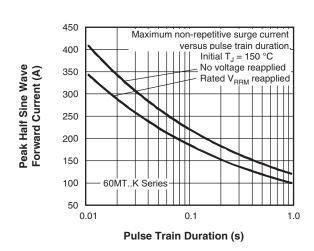


Fig. 5 - Maximum Non-Repetitive Surge Current

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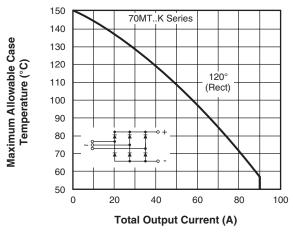


Fig. 6 - Current Ratings Characteristics

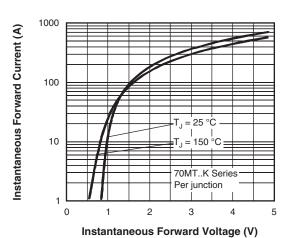
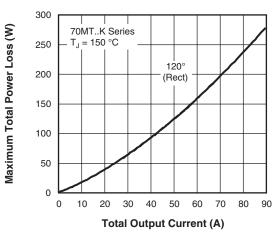


Fig. 7 - Forward Voltage Drop Characteristics



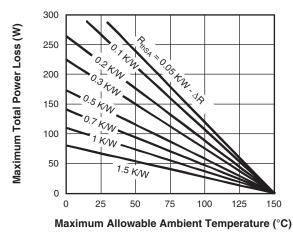


Fig. 8 - Total Power Loss Characteristics

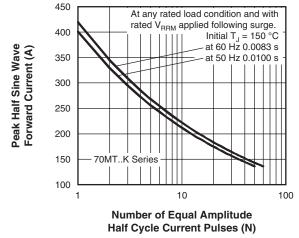


Fig. 9 - Maximum Non-Repetitive Surge Current

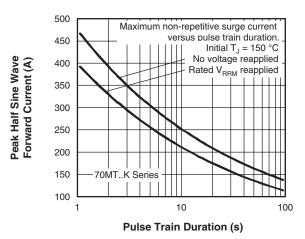


Fig. 10 - Maximum Non-Repetitive Surge Current



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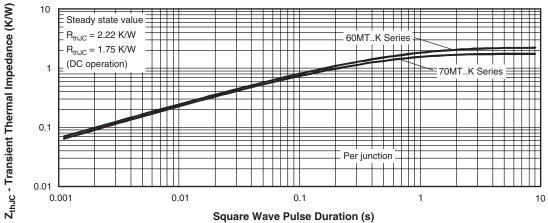
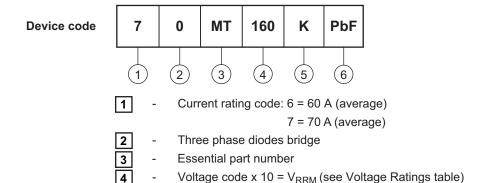


Fig. 11 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

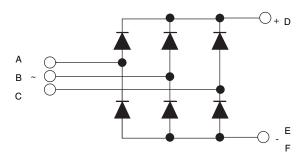


PbF = Lead (Pb)-free

Note

• To order the optional hardware go to www.vishay.com/doc?95172

CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95004			

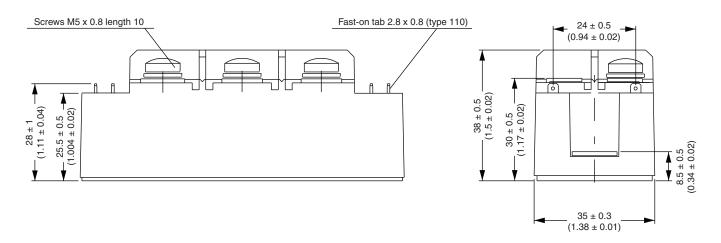
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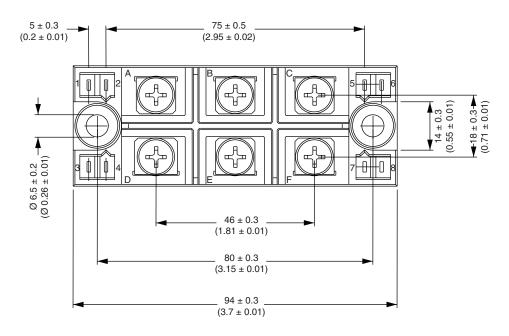


Vishay Semiconductors

MTK (with and without optional barrier)

DIMENSIONS WITH OPTIONAL BARRIERS in millimeters (inches)

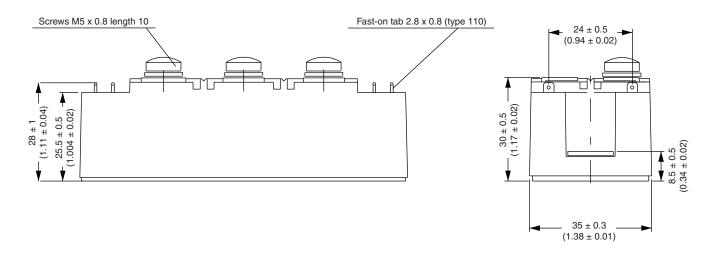


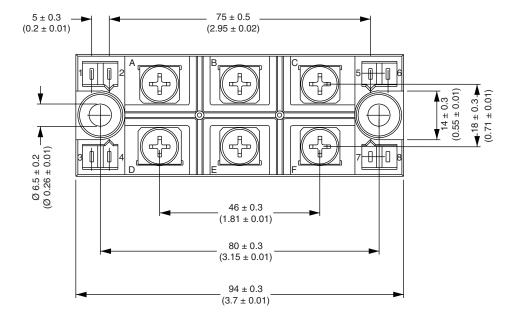


Vishay Semiconductors MTK (with and without optional barrier)



DIMENSIONS WITHOUT OPTIONAL BARRIERS in millimeters (inches)







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