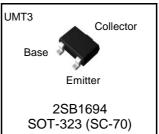


PNP -1A -30V Low Frequency Amplifier Transistors

Parameter	Value
- i didiffolor	value
$V_{\sf CEO}$	-30V
Ic	-1A

●Outline



Features

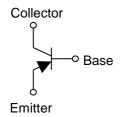
- 1) A Collecotr current is large. General Purpose.
- 2) Collector saturation voltage is low.

$$V_{\text{CE(sat)}}$$
 is Max. -380mV

At
$$I_C = -500$$
mA, $I_B = -25$ mA

- 3) Complementary NPN Types : 2SD2656
- 4) Lead Free/RoHS Compliant.

•Inner circuit



Applications

Driver circuit

Packaging specifications

Part No.	Package	Package size (mm)	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit (pcs)	Marking
2SB1694	UMT3	2021	T106	180	8	3,000	ES

● Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Values	Unit
Collector-base voltage	V _{CBO}	-30	V
Collector-emitter voltage	V _{CEO}	-30	V
Emitter-base voltage	V_{EBO}	-6	V
Collector current	I _C	-1	Α
Collector current	I _{CP} *1	-2	Α
Power dissipation	P _D *2	200	mW
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	−55 to +150	°C

●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Collector-emitter breakdown voltage	BV _{CEO}	$I_C = -1mA$	-30	ı	-	V
Collector-base breakdown voltage	BV _{CBO}	$I_C = -10\mu A$	-30	ı	ı	V
Emitter-base breakdown voltage	BV _{EBO}	$I_E = -10\mu A$	- 6	ı	ı	V
Collector cut-off current	I _{CBO}	$V_{CB} = -30V$	ı	ı	-100	nA
Emitter cut-off current	I _{EBO}	$V_{EB} = -6V$	-	-	-100	nA
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C = -500 \text{mA}, I_B = -25 \text{mA}$	ı	-180	-380	mV
DC current gain	h _{FE}	$V_{CE} = -2V, I_{C} = -100 \text{mA}^{*3}$	270	ı	680	-
Transition frequency	f _T	$V_{CE} = -2V, I_{E} = 100 \text{mA}$ f=100MH _Z ^{*3}	ı	320	-	MHz
Output capacitance	C _{ob}	$V_{CB} = -10V$, $I_E = 0mA$ f = 1MHz	ı	7	-	pF

^{*1} P_W=1ms Single pulse.

^{*2} Each terminal mounted on a reference footprint

^{*3} Pulsed

●Electrical characteristic curves(Ta = 25°C)

Fig.1 Ground Emitter Propagation Characteristics

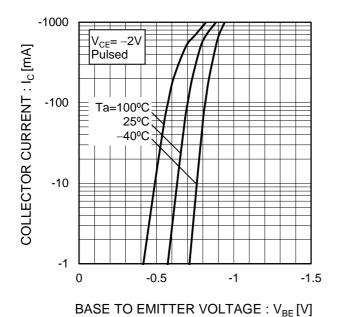
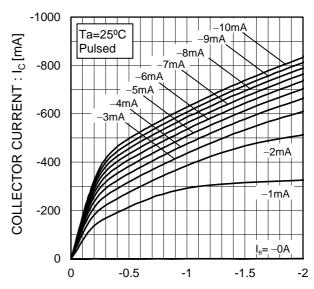


Fig.2 Typical Output Characteristics



COLECTOR TO EMITTE VOLTAGE : V_{CE}[V]

Fig.3 DC Current Gain vs. Collector Current(I)

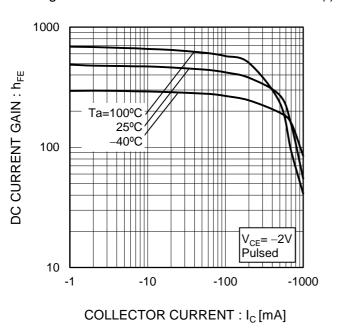
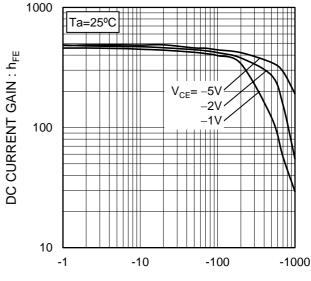
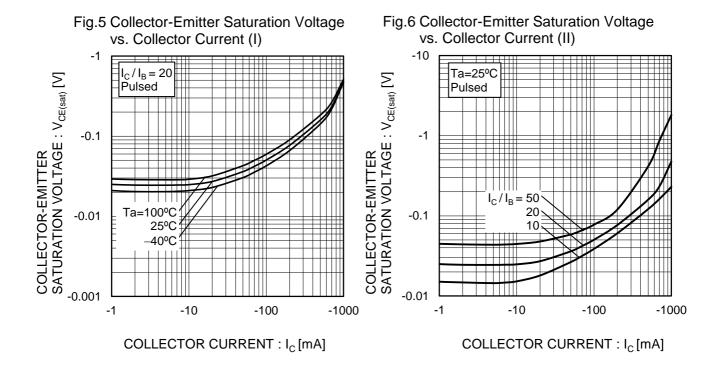


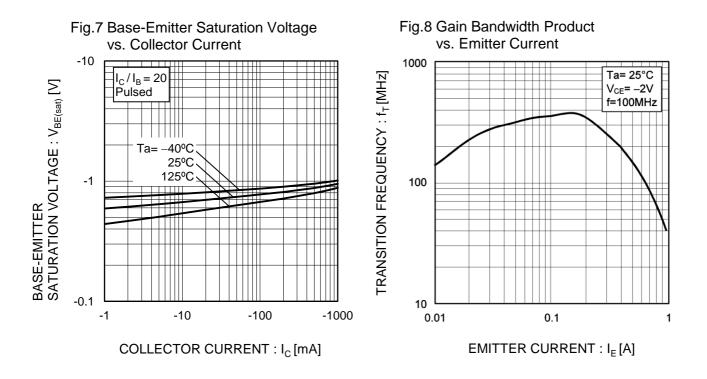
Fig.4 DC Current Gain vs. Collector Current(II)



COLLECTOR CURRENT : I_C [mA]

●Electrical characteristic curves(Ta = 25°C)





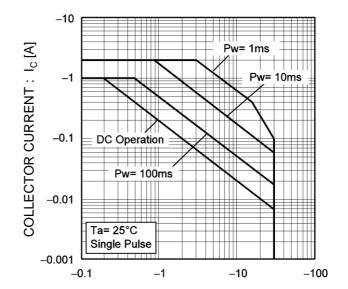
●Electrical characteristic curves(Ta = 25°C)

Fig.9 Emitter input capacitance vs.
Emitter-Base Voltage
Collector output capacitance vs.
Collector-Base Voltage

100

Cobbound C

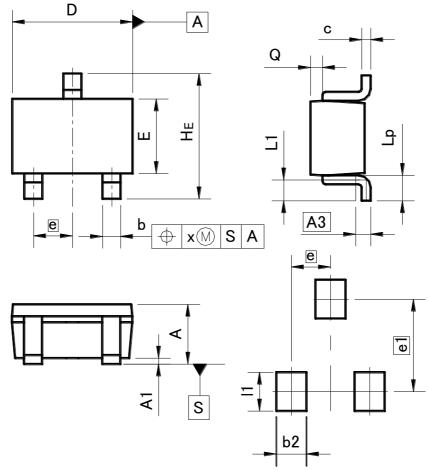
Fig.10 Safe Operating Area



COLLECTOR TO EMITTER VOLTAGE : $V_{CE}\left[V\right]$

●Dimensions (Unit : mm)

UMT3



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INCHES		
DIIVI	MIN	MAX	MIN	MAX	
Α	0.80	1.00	0.031	0.039	
A1	0.00	0.10	0.000	0.004	
A3	0.3	25	0.0	10	
b	0.15	0.30	0.006	0.012	
С	0.10	0.20	0.004	0.008	
D	1.90	2.10	0.075	0.083	
E	1.15	1.35	0.045	0.053	
е	0.65		0.0	26	
HE	2.00	2.20	0.079	0.087	
L1	0.20	0.50	0.008	0.020	
Lp	0.25	0.55	0.010	0.022	
Q	0.10	0.30	0.004	0.012	
Х	_	0.10	_	0.004	

DIM	MILIMETERS		INCHES		
DIIVI	MIN	MAX	MIN	MAX	
b2	-	0.50	ı	0.020	
e1	1.55		0.0	161	
l1	-	0.65	-	0.026	

Dimension in mm / inches

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