Vishay High Power Products

Schottky Rectifier, 120 A



- 150 °C T_J operation
- · Low forward voltage drop
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- · Lead (Pb)-free
- · Designed and qualified for industrial level

DESCRIPTION

The 120NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	120	A		
V _{RRM}		45	V		
I _{FSM}	$t_p = 5 \ \mu s \ sine$	26 000	A		
V _F	120 Apk, T _J = 125 °C	0.62	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	120NQ045PbF	UNITS		
Maximum DC reverse voltage	V _R	45	V		
Maximum working peak reverse voltage	V _{RWM}	- 40 V			

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_{C} = 105 °C, rectangular waveform		120	
Maximum peak one cycle non-repetitive surge current	1	5 μs sine or 3 μs rect. pulse Following any rated	26 000	A	
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse		1550	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 13 \text{ A}, L = 1 \text{ mH}$		81	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		13	А

PRODUCT SUMMARY

I_{F(AV)}

 V_{R}

SHA



Lug terminal

anode

C

6

120 A

45 V

HALF-PAK (D-67)



120NQ045PbF

RoHS

COMPLIANT



120NQ045PbF

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		120 A	T.I = 25 °C	0.63	
Maximum forward voltage drop	V _{FM} ⁽¹⁾	240 A	1)=25 C	0.86	v
See fig. 1	V FM (''	120 A	T 105 %C	0.62	v
		240 A	T _J = 125 °C	0.81	
Maximum reverse leakage current See fig. 2	I (1)	T _J = 25 °C	V _R = Rated V _R	10	mA
	I _{RM} ⁽¹⁾	T _J = 125 °C		500	
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		5200	pF
Typical series inductance	Ls	From top of terminal hole to mounting plane		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 500 μs

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C	
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	0.38 °C/W		
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.05	0/11	
Approximate weight				30	g	
Approximate weight				1.06	oz.	
Mounting torque	minimum		Non-lubricated threads	3 (26.5)		
Mounting torque	maximum			4 (35.4)	N ⋅ m (lbf ⋅ in)	
Terminal terring	minimum			3.4 (30)		
Terminal torque	maximum			5 (44.2)		
Case style				HALF-PA	K module	



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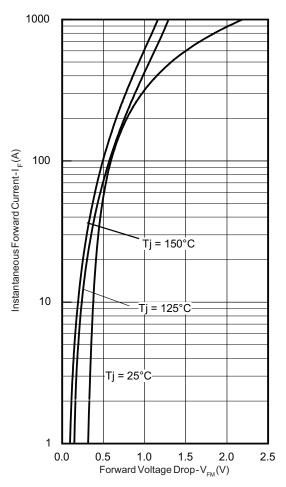


Fig. 1 - Maximum Forward Voltage Drop Characteristics

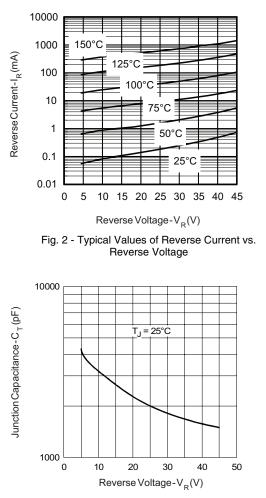


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

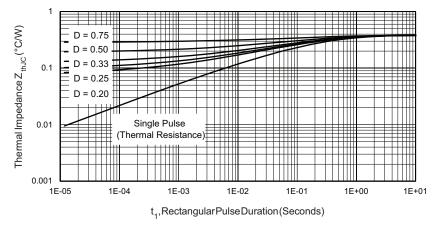
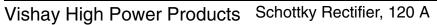
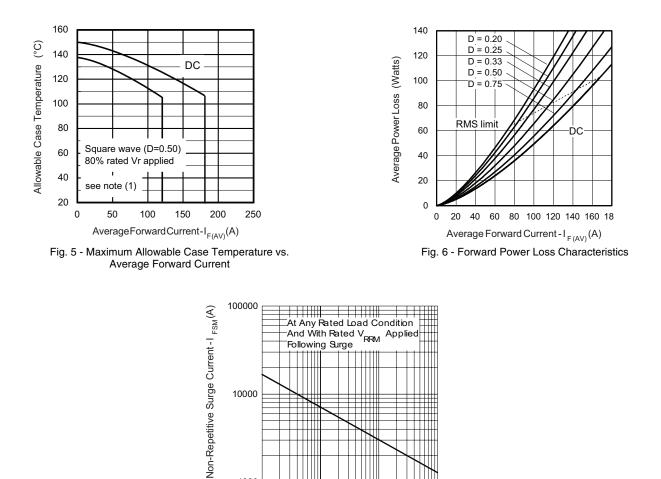
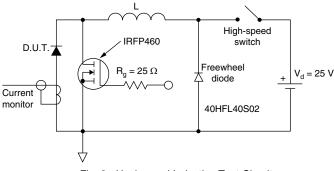


Fig. 4 - Maximum Thermal Impedance $Z_{thJC} \mbox{ Characteristics}$

120NQ045PbF







100

Square Wave Pulse Duration - t _p (microsec) Fig. 7 - Maximum Non-Repetitive Surge Current

1000

10000

1000 └ 10

Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽⁹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

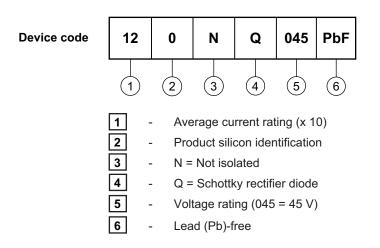
 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (1 - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{Rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

VISHA



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS			
Dimensions	http://www.vishay.com/doc?95020		

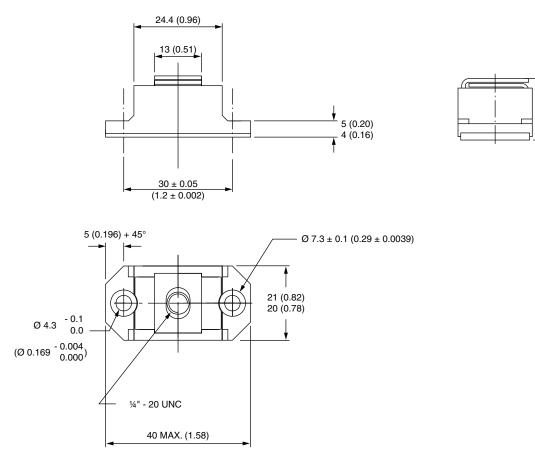
Vishay Semiconductors

17.5 (0.69) 16.5 (0.65)



DIMENSIONS in millimeters (inches)

SHAY





Vishay

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