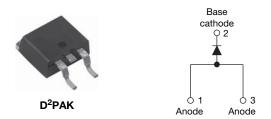


Diode variation

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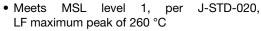
High Voltage Surface Mount Input Rectifier Diode, 10 A

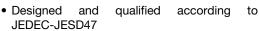


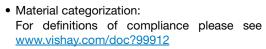
PRODUCT SUMMARY	
Package	TO-263AB (D ² PAK)
I _{F(AV)}	10 A
V_{R}	800 V, 1000 V, 1200 V
V _F at I _F	1.1 V
I _{FSM}	160 A
T _i max.	150 °C

Single die

FEATURES











ROHS COMPLIANT HALOGEN FREE

APPLICATIONS

- · Input rectification
- Vishay switches and output rectifiers which are available in identical package outlines

DESCRIPTION

The VS-10ETS..SPbF rectifier series has been optimized for very low forward voltage drop, with moderate leakage. The glass passivation technology used has reliable operation up to 150 °C junction temperature.

OUTPUT CURRENT IN TYPICAL APPLICATIONS						
APPLICATIONS SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS						
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C common heatsink of 1 °C/W	12.0	16.0	А			

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL CHARACTERISTICS VALUES UNITS							
I _{F(AV)}	Sinusoidal waveform	10	А				
V _{RRM}		800/1200	V				
I _{FSM}		160	A				
V _F	10 A, T _J = 25 °C	1.1	V				
T _J		- 40 to 150	°C				

VOLTAGE RATINGS						
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA			
VS-10ETS08SPbF	800	900				
VS-10ETS10SPbF	1000	1100	0.5			
VS-10ETS12SPbF	1200	1300				

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum average forward current	I _{F(AV)}	$T_C = 105$ °C, 180° conduction half sine wave	10			
Maximum peak one cycle		10 ms sine pulse, rated V _{RRM} applied	135	А		
non-repetitive surge current	IFSM	10 ms sine pulse, no voltage reapplied	160			
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied 91		A ² s		
Maximum i-t for fusing	1-1	10 ms sine pulse, no voltage reapplied	130	A-S		
Maximum I ² √t for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied	1290	A ² √s		





ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL TEST CONDITIONS VALUES UNI					
Maximum forward voltage drop	V _{FM}	10 A, T _J = 25 °C	1.1	V		
Forward slope resistance	r _t	T _{.1} = 150 °C	20	mΩ		
Threshold voltage	V _{F(TO)}	1) = 150 C	0.82	V		
Maximum various lasks as a uniont	I _{RM}	T _J = 25 °C	V Dated V	0.05	A	
Maximum reverse leakage current		T _J = 150 °C	V _R = Rated V _{RRM}	0.50	- mA	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range	T _J , T _{Stg}		- 40 to 150	°C		
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	2.5	°C/W		
Maximum thermal resistance, junction to ambient (PCB mount)	R _{thJA} (1)		62	-0/00		
Soldering temperature	T _S		260	°C		
Approximate weight			2	g		
Approximate weight			0.07	OZ.		
			10ET	S08S		
Marking device		Case style D ² PAK (SMD-220)	10ET	S10S		
			10ET:	S12S		

Note

⁽¹⁾ When mounted on 1" square (650 mm²) PCB of FR-4 or G-10 material 4 oz. (140 μm) copper 40 °C/W For recommended footprint and soldering techniques refer to application note #AN-994

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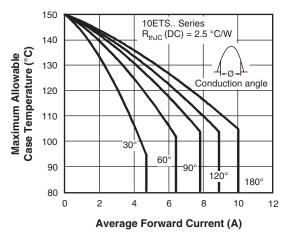


Fig. 1 - Current Rating Characteristics

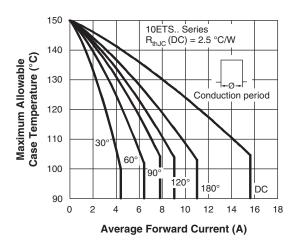


Fig. 2 - Current Rating Characteristics

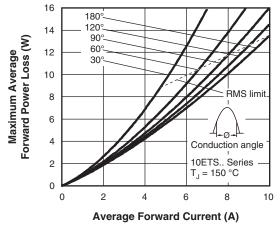


Fig. 3 - Forward Power Loss Characteristics

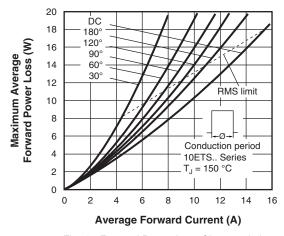


Fig. 4 - Forward Power Loss Characteristics

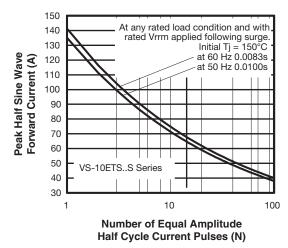


Fig. 5 - Maximum Non-Repetitive Surge Current

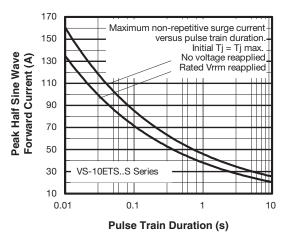


Fig. 6 - Maximum Non-Repetitive Surge Current

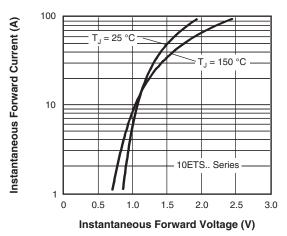


Fig. 7 - Forward Voltage Drop Characteristics

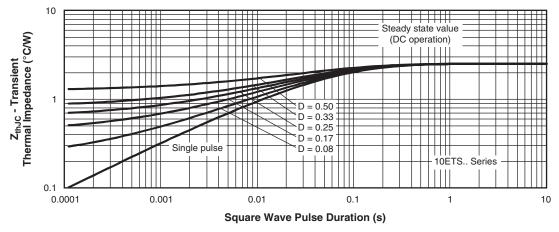
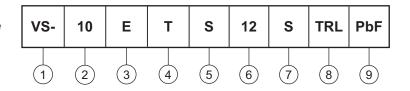


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semicondutors product

2 - Current rating (10 = 10 A)

3 - Circuit configuration:

E = Single diode

4 - Package:

T = TO-220AC

5 - Type of silicon:

S = Standard recovery rectifier

08 = 800 V

6 - Voltage code x 100 = V_{RRM}

10 = 1000 V

7 - S = TO-220 D²PAK (SMD-220) version

12 = 1200 V

8 - • None = Tube

• TRL = Tape and reel (left oriented)

• TRR = Tape and reel (right oriented)

9 - PbF = Lead (Pb)-free

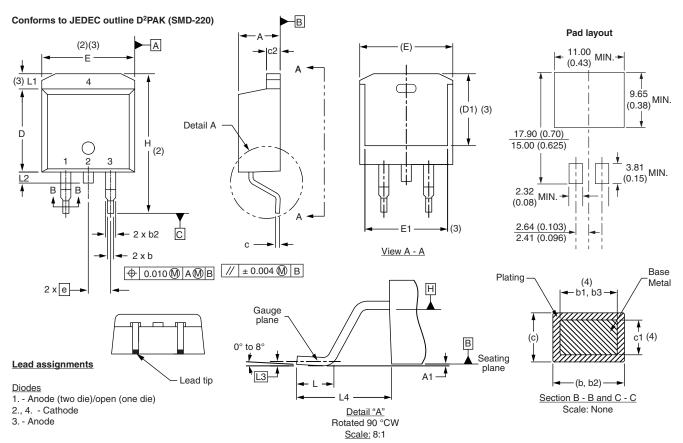
ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-10ETS08SPbF	50	1000	Antistatic plastic tube			
VS-10ETS08STRRPbF	800	800	13" diameter reel			
VS-10ETS08STRLPbF	800	800	13" diameter reel			
VS-10ETS10SPbF	50	1000	Antistatic plastic tube			
VS-10ETS10STRRPbF	800	800	13" diameter reel			
VS-10ETS10STRLPbF	800	800	13" diameter reel			
VS-10ETS12SPbF	50	1000	Antistatic plastic tube			
VS-10ETS12STRRPbF	800	800	13" diameter reel			
VS-10ETS12STRLPbF	800	800	13" diameter reel			
VS-10ETS08SPbF	50	1000	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95046			
Part marking information	www.vishay.com/doc?95054			
Packaging information	www.vishay.com/doc?95032			



D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INCHES		NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MILLIM	ETERS	INCHES		NOTES	
STWBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
D1	6.86	8.00	0.270	0.315	3	
E	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54 BSC		0.100 BSC			
Н	14.61	15.88	0.575	0.625		
L	1.78	2.79	0.070	0.110		
L1	-	1.65	1	0.066	3	
L2	1.27	1.78	0.050	0.070		
L3	0.25 BSC		0.010	BSC		
L4	4.78	5.28	0.188	0.208		

Notes

- $^{(1)}$ Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC outline TO-263AB



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