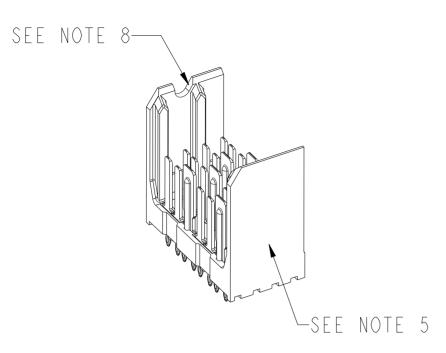
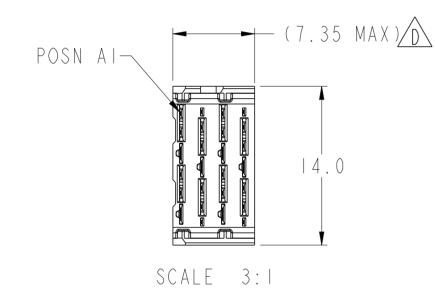
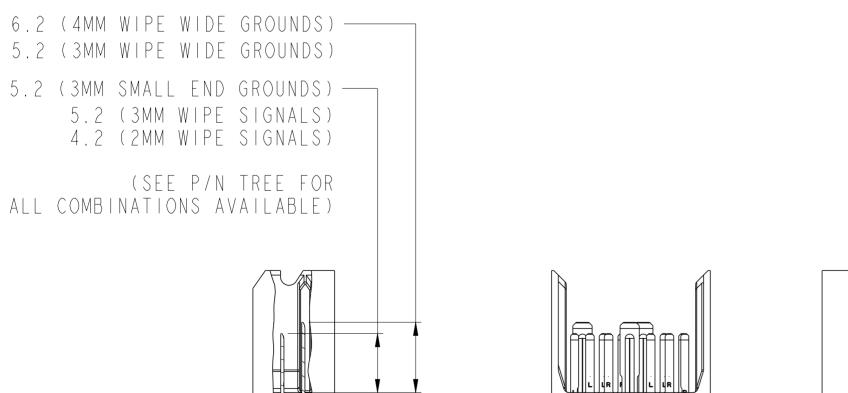
	2	3	4	5	6		7	8	
						PERFOR	RMANCE-BASED PI	LATING (SEE NOTE 2)	
		\bigcirc	\vee \cap \vee $ $				APPLICATION	LEAD FREE (LF)	
	0 4868 - X					B TEL	CORDIA CO	NO	
A				LEAD-FREE DESIGNAT			CORDIA UE	NO	A
		I 4 - COLUMN		ONLY WITH APPLICAB PLATING CODES	LE		CORDIA CO	YES	_
		DIFFERENTIAL SIGNAL		TEATING CODES		E TEL	CORDIA UE	YES	_
		OTOWAL							-
						MBER 0 STANDARD	PIN STYLE, D GUIDE PIN MAC		
						U JIANDARL	J GUIDE FIN MAC	UNED, 20.7000	
В	MODULE DESCRIPTION LETTER DES	IGNATION REPRESENTED IN	DASH NUMBER	B A S E MODULE			ING WIPE LENGTH		
			E G		NUMBER			COMPLIANT PIN DRILL SIZE	
	LEFT J A B	C D E	F G	H		SIGNAL	WIDE GROUND		_
	POLARIZING B A H B A H B A H B	A H B A H B A	H B A H B A H I			2	<u>4</u>	\emptyset 0.55 [0.0217"] \emptyset 0.55 [0.0217"]	_
	GUIDANCE MODULE					2	4	$\emptyset 0.35 [0.0217]$ $\emptyset 0.45 [0.0177"]$	-
	(SEE SHEET 3) KEY F D F D F D					3	4	\emptyset 0.45 [0.0177"]	_
		E E E			5	2	3	\emptyset 0.55 [0.0217"]	_
С	Y P Q	R S T	UV	W	6	3	3	Ø0.55 [0.0217"]	С
	RIGHT A A A		Α Α		7	2	3	Ø0.45 [0.0177"]	
	POLARIZING GUIDANCE BAHBAHBAHBAHBA	H B H B	H B H B H I		8	3	3	Ø0.45 [0.0 77"]	
	MODULE (SEE SHEET 4)						-		
		F D F D F	F D F F D F F	D F F	TAB	LE I: OPTION	NAL PCB HOLE S	IZE (SEE NOTE II)	
				L			GROUNDED	UNGROUNDED	
	OPEN MODULE				FIN	ISHED	Ø2.35-2.55	Ø2.35-2.55	
D	(TWO WALL) (SEE SHEET 2)	0 (ZERO)				PAD	Ø5.50	N / A	D
				and the second	PL	ATED	YES	NO	
						RILL	2.58 /.1015	2.44 /.096	
	LEFT WALL MODULE	L				m/in)			_
	(SEE SHEET 5)	_			Т	ABLE 2: OPTI	ONAL SCREW TYP	E (SEE NOTE IO)	
						EW P/N	DESCRIPTION	BOARD THICKNESS	_
E	RIGHT WALL						#2-56 X .375"		E
	MODULE (SEE SHEET 6)	M			100917	90-002LF P	'HILLIPS PANHEA	D I.60 - 5.00	
L				Brown when a second			SQ CONICAL WASH		_
							#2-56 X .500" "HILLIPS PANHEA		
FCI.	FOUR WALL MODULE	1					SQ CONICAL WASH		
ight	(SEE SHEET 7)	I							
Copyr				spec ref SEE NO	TES dr Kok-Hoe Lee	2010/06/25	projection N/N	A size scale	_
F				tolerance std TOLER.	ANCES UNLESS	2011/11/23		A 2 2 : 1 ecn no ELX-S-008191-1	_ F
				ASME YI4.5 OTHERW	ISE SPECIFIED appr Chen-Hong Tan		oduct family	XCede rel level Released	
				surface - linear	0.XX ±0.10	MODULE ASSEME Vertical header, 2		ౖ ఈ 0 4868 రా	
	Pro/E File - REV C - 2009-06-09			ASME YI4.5 angular	0° ±° www.fci.com co	t. no. SEE P/N	TREE Product - C	ustomer Drw sheet 1 of 8	
	2	3	4	5	PDS: Rev :D	STAT	US:Released	Printed: Nov 24, 2011	

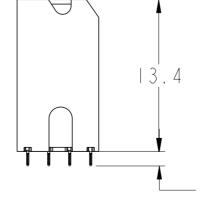
	2	3	4	
OPEN (2	-WALL) MODULE			



2.06 TO TOP SURFACE OF DAUGHTERCARD







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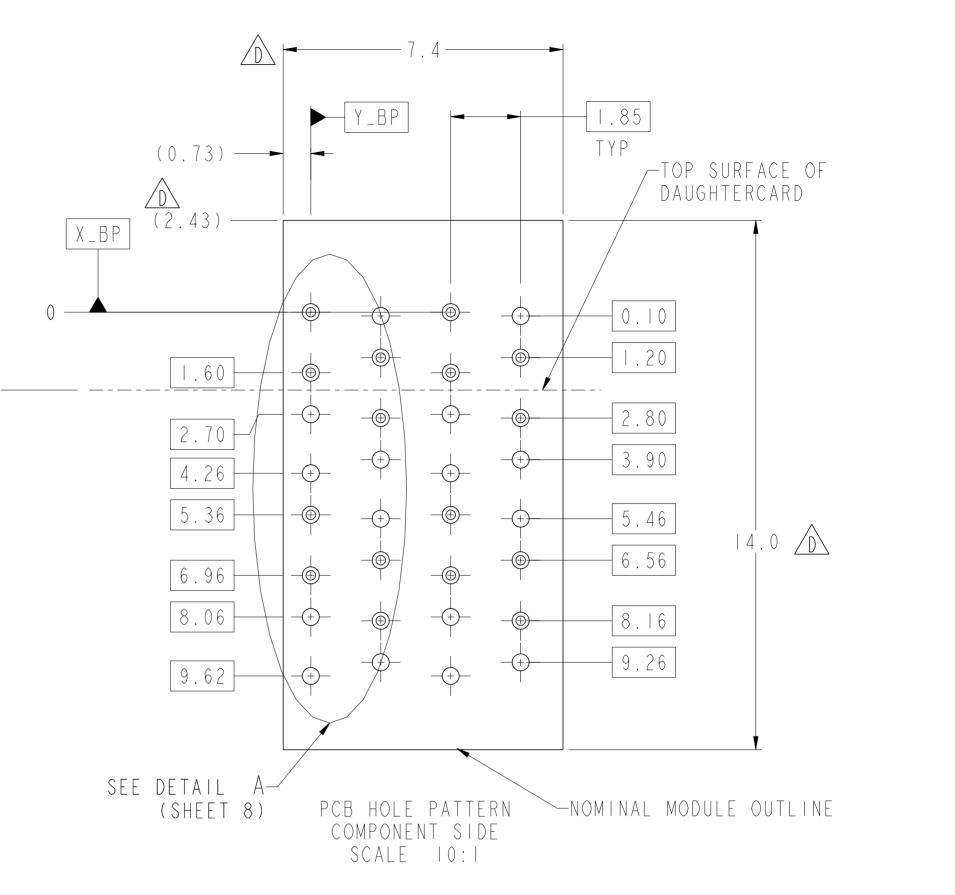
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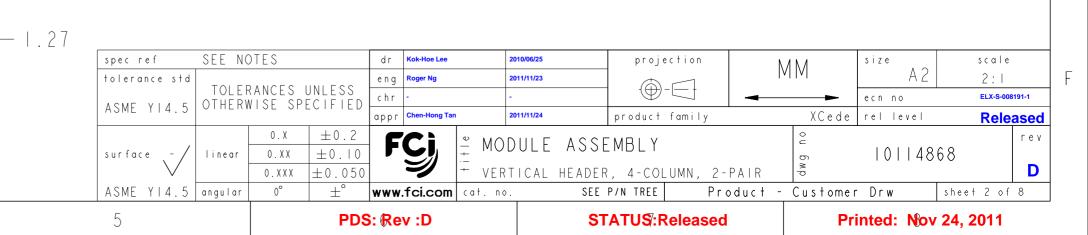
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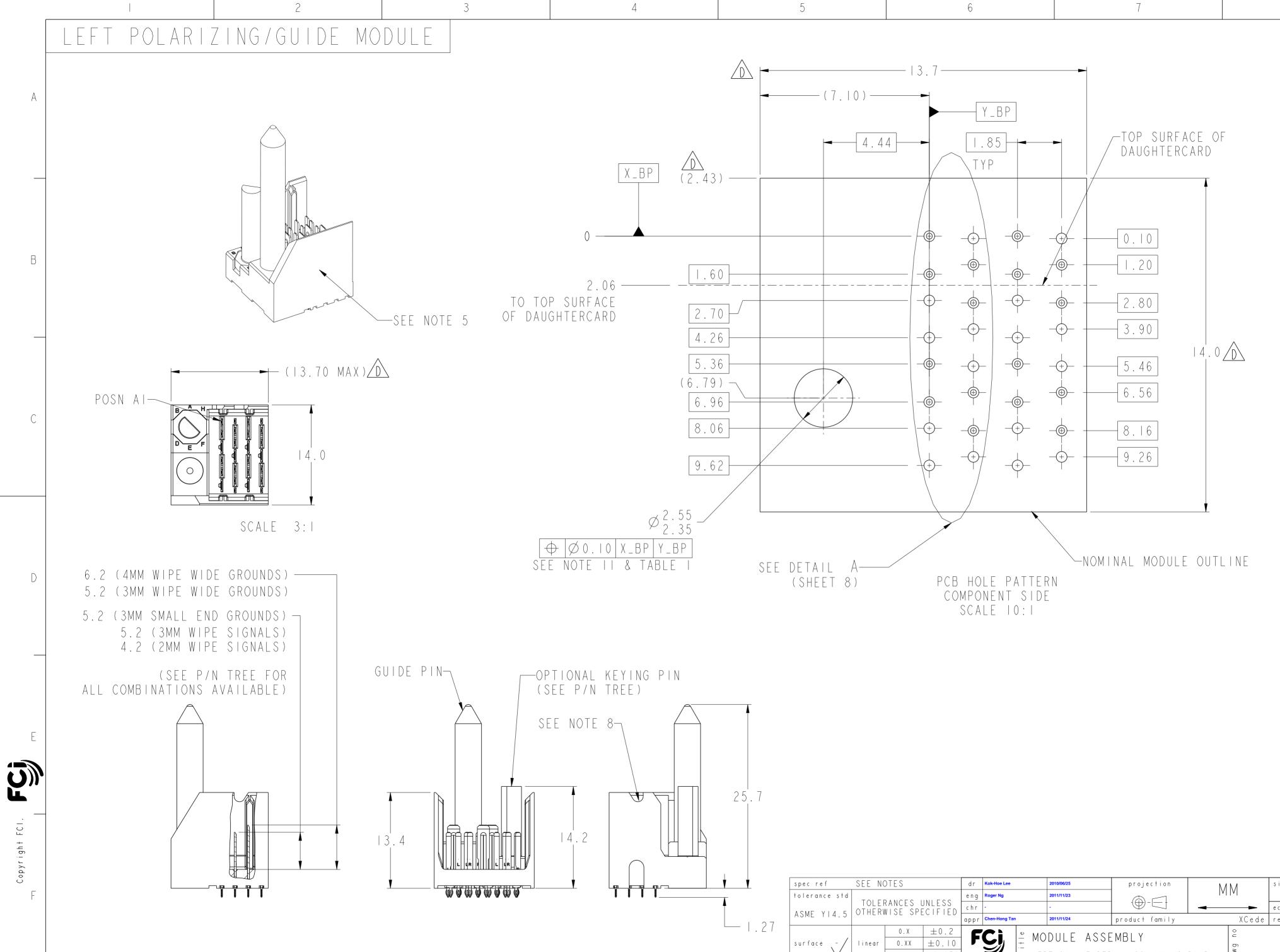
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<u> </u>	spec ref	SEE NO	DTES		dr	Kok-Hoe Lee		2010/06/25	proje	ection	Ι	4 M	size	scale	:	
	tolerance std				eng	Roger Ng		2011/11/23		\square		/ v	A 2	2:1		F
			RANCES I Vise spi	UNLESS ECIFIED	chr	-		-	Ð		-	►	ecn no	ELX-S-00	18191-1	
	ASME YI4.5				appr	Chen-Hong Tan		2011/11/24	product	family		XCede	rel level	Rele	eased	
Ι.ΖΙ			0.X	±0.2	C		– MO	DULE ASSE				0 U			rev	
	surface -	linear	0.XX	±0.10			— IVI∪ —	DULL ASSL				Ø	101148	68		
			0.XXX	\pm 0.050		Y	⁺ VER	TICAL HEADER	, 4-COL	UMN, 2-	- P A I R	d M			D	
	ASME YI4.5	angular	0°	±°	www.	fci.com	cat. no	. SEE	P/N TREE	Pr	oduct -	Customer	Drw	sheet 3 of	8	
	5			PDS	: Rev	v :D		ST		leleased	k	Pri	nted: Nov	24, 2011		

RIGHT POLARIZING/GUIDE MODULE

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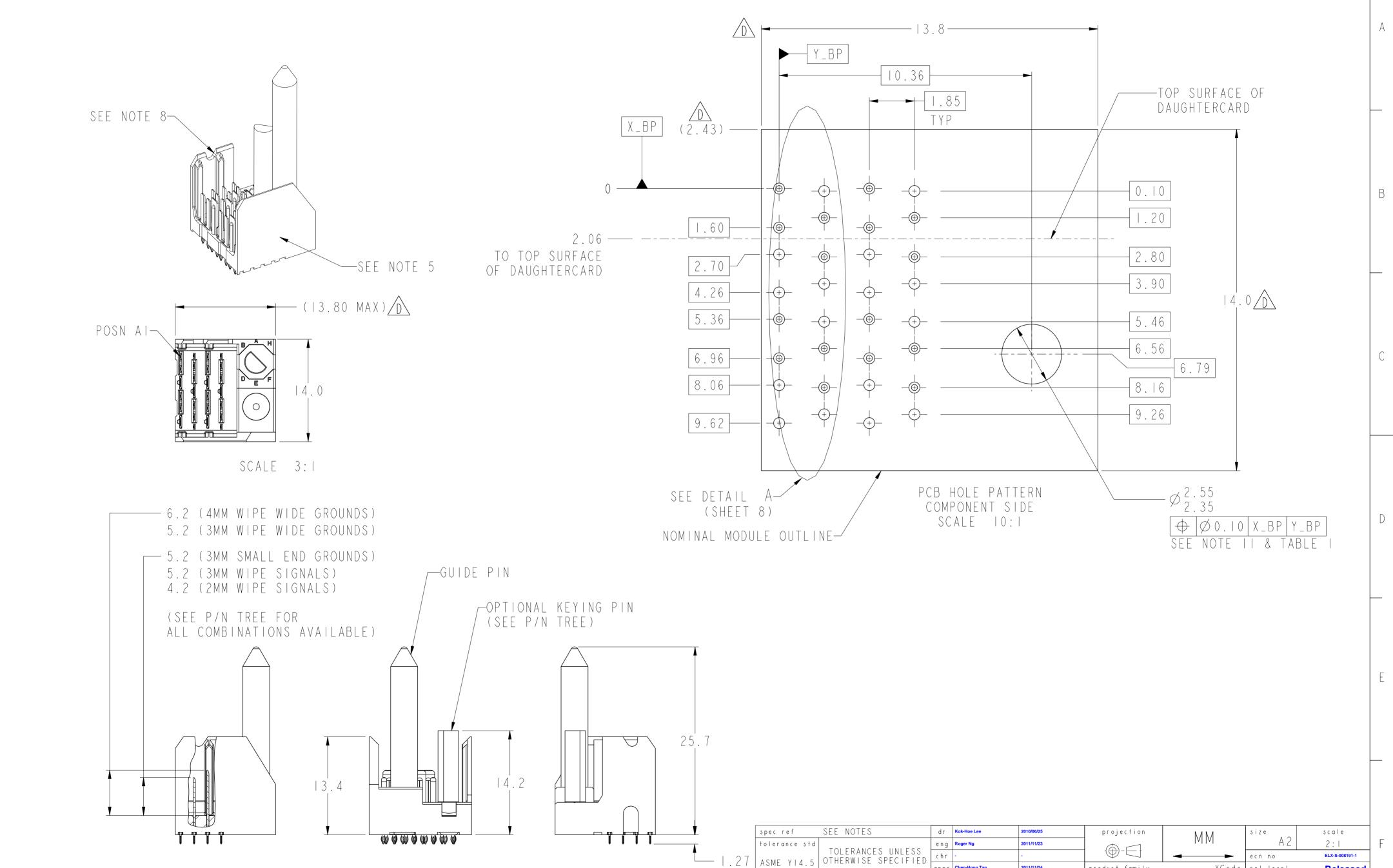
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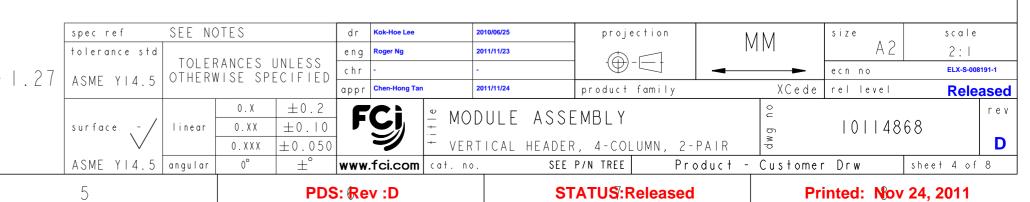
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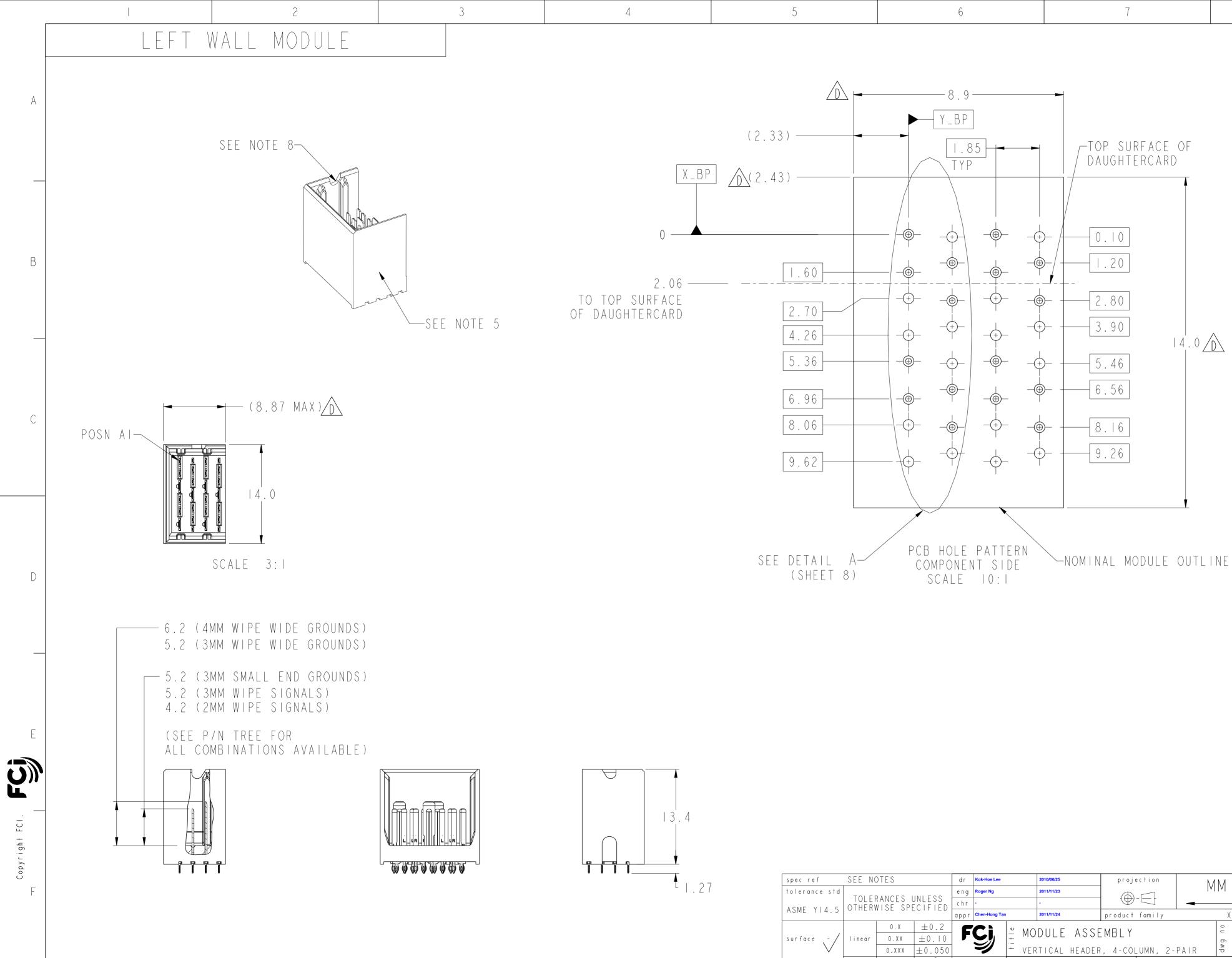


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		0.X	±0.2	C		- MO[DULE ASSI				0 0			rev
surface _/	linear	0.XX	±0.10				JULE ASSI				Di Di	101148	68	
		0.XXX	± 0.050		I I	⁺ VER	TICAL HEADEF	R, 4-COL	_UMN, 2-	PAIR	a q q			D
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5			PDS	: Re	v :D		SI	TATUS:F	Released		Pri	nted: Nov	24, 201 ⁻	1

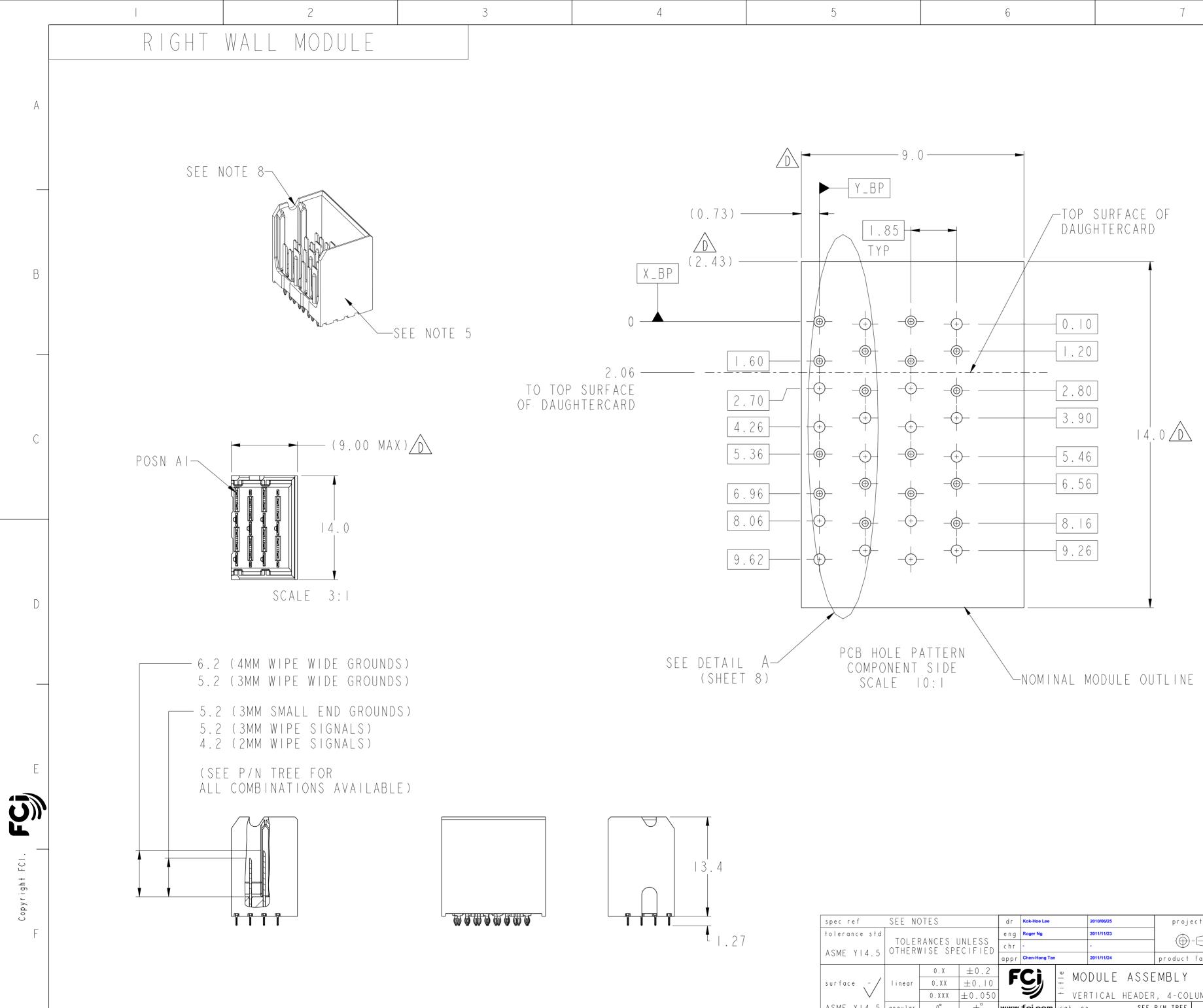
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ASME YI4.5				appr	Chen-Hong Tan	2	011/11/24	product	family		XCede	rel level	Rele	ased
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\vee		0.XXX	\pm 0.050		Y	+ VERT	ICAL HEADER	, 4-COL	UMN, 2-	PAIR	v S			D
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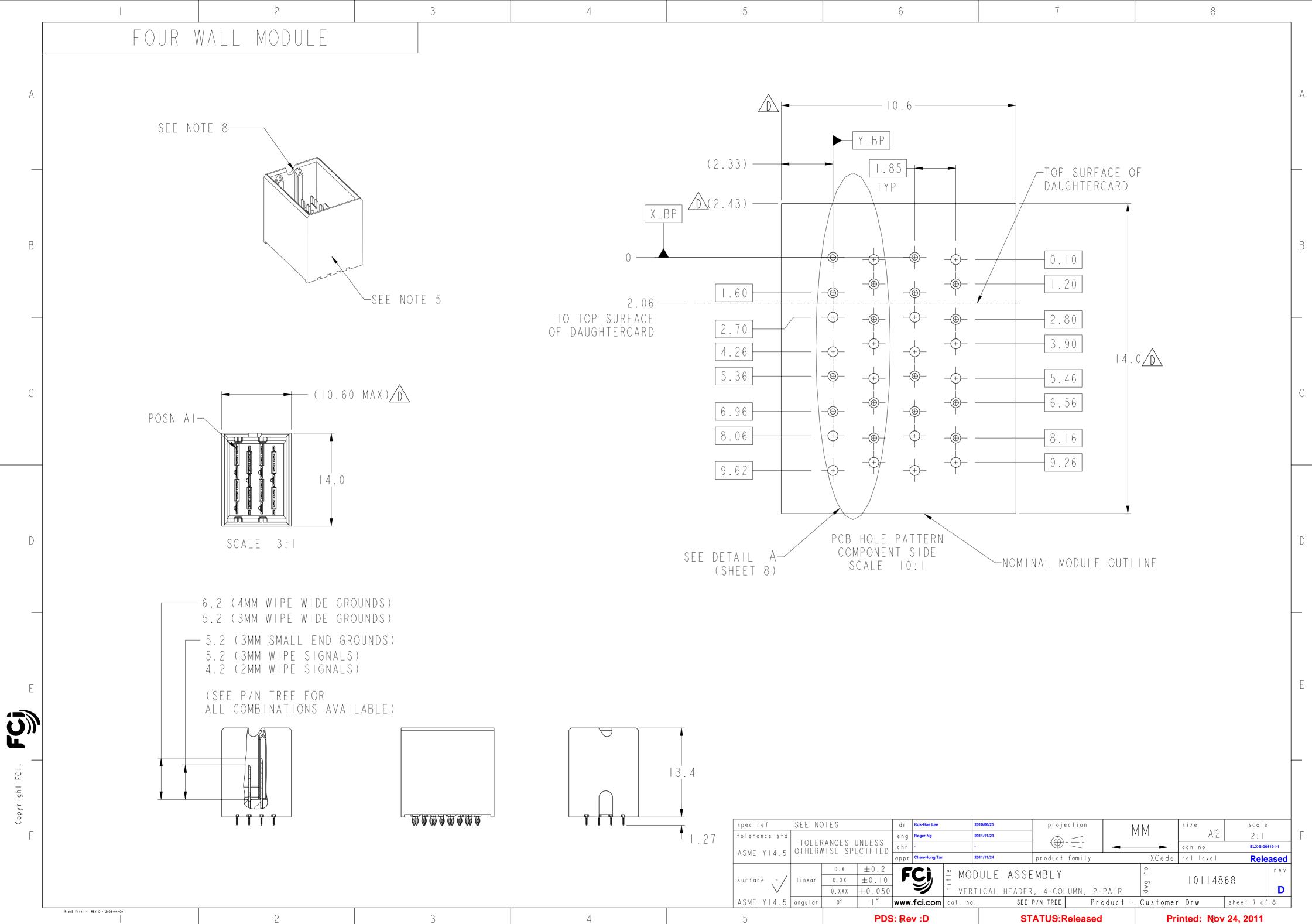
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	NOTES:
	I. CONNECTOR MATERIALS SHROUD: HIGH-TEMP THERMOPLASTIC, GRAY, UL94V-0 CONTACTS: HIGH PERFORMANCE COPPER ALLOY
	2. CONTACT PLATING:
В	SEPARABLE INTERFACE: PERFORMANCE-BASED PLATING, QUALIFIED TO MEET THE REQUIREMENTS OF FCI PRODUCT SPECIFICATION GS-12-588, INCLUDING TELCORDIA GR-1217-CORE (NOVEMBER 1995) CENTRAL OFFICE TEST (CO) OR UNCONTROLLED ENVIRONMENT TEST SEQUENCE AS NOTED IN P/N TREE.
	PRESS-FIT TAILS: TIN OVER NICKEL, OR TIN-LEAD ALLOY OVER NICKEL. SEE P/N TREE.
	3. PRODUCT SPECIFICATION: GS-12-588
	4. APPLICATION SPECIFICATION: GS-20-121
С	(5.) PRODUCT MARKING (PART NUMBER AND LOT CODE) ON THESE SURFACES.
	6. HOUSING MUST WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 40 SECONDS IN A CONVECTION, INFRA-RED, OR VAPOR-PHASE REFLOW OVEN.
	7. PACKAGING MEETS GS-14-920 LEAD-FREE LABELING SPECIFICATION.
	8. NOTCH INDICATES "ROW A" SIDE OF CONNECTOR.
	(9.) SEE APPLICATION SPECIFICATION FOR ROUTING GUIDELINES, ADDITIONAL PTH GUIDELINES, MATED DIMENSIONS, GUIDE PN SELECTION, ETC.
D	(10) SCREW IS OPTIONAL AND ENGAGES WITH BOTTOM OF GUIDE-PIN FROM BENEATH THE PCB FOR ADDITIONAL GUIDE-PIN SUPPORT.
	(I) OPTIONAL HOLE LOCATION FOR GROUNDED PIN OR ADDITIONAL GUIDE-PIN SUPPORT. SEE TABLE I ON SHEET I FOR ADDITIONAL DETAILS.
	12. A $\widehat{\mathbb{D}}$ symbol will be next to any dimension, view, or note which has been modified with the current drawing revision.
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