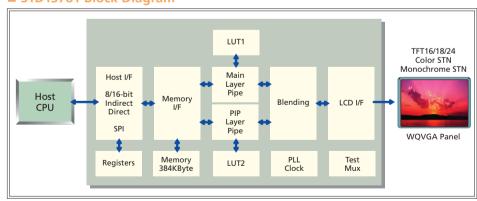
### **■ LCD Controller with Built-in VRAM**

A single-chip LCD controller with built-in display memory allowing for low power consumption, low noise, and space-saving ability. This product is most suitable for the display control of mobile terminals and operation panels.

|                              |  | LCD               | Interfa                      | ce Sup                         | ort                   | Color Depth  | Internal           | Supply         | Voltage        | Additional                                  |                            |
|------------------------------|--|-------------------|------------------------------|--------------------------------|-----------------------|--|--------------------|----------------|----------------|---|----------------------------|
| Product                      | CPU Interface Support  | Monochrome<br>STN | Color<br>STN                 | TFT                            | Typical<br>Resolution | (Max.)   | Memory<br>Capacity | Core           | Ю              | Features                                    | Package                    |
| S1D13700F02A                 | 8-bit I/F,<br>Direct addressing<br>Indirect addressing               | 4-bit             | n/a                          | n/a                            | QVGA                  | 16 grayscale   | 32KB,<br>SRAM      | 3.0V to 3.6V   | 3.0V to 5.5V   | 3 overlay screens                           | TQFP13-64                  |
| \$1D13705F00A                | 8-bit I/F (with external logic)<br>16-bit I/F,<br>Direct addressing  | 4-bit / 8-bit     | 4-bit /<br>8-bit             | 9-bit /<br>12-bit              | QVGA                  | MSTN:16 grayscale<br>CSTN:256 colors<br>TFT:256 colors | 80KB,<br>SRAM      | 2.7V to 3.6V   | 2.7V to 5.5V   | SwivelView                                  | QFP14-80                   |
| S1D13706F00A                 | 8-bit I/F(with external logic),<br>16-bit I/F,<br>Direct addressing  | 4-bit / 8-bit     |                              |                                | QVGA                  | MSTN:64 grayscale<br>CSTN:64K colors<br>TFT:64K colors | 80KB,<br>SRAM      | 1.8V to 3.6V   | 1.8V to 3.6V   | SwivelView,<br>PinP                         | TQFP15-100                 |
| S1D13742F01A                 | 8-bit / 16-bit I/F<br>Indirect addressing                            | n/a               | n/a                          | 18-bit                         | VGA                   | 256K colors  | 768KB,<br>SRAM     | 1.4V to 1.6V   | 1.65V to 3.60V | SwivelView                                  | QFP20-144                  |
| S1D13743F00A                 | 8-bit / 16-bit I/F<br>Indirect addressing                            | n/a               | n/a                          | 18-bit /<br>24-bit             | WQVGA                 | 16M colors   | 464KB,<br>SRAM     | 1.4V to 1.6V   | 1.65V to 3.60V | SwivelView                                  | QFP20-144                  |
| S1D13748F00A<br>S1D13748B00B | 16-bit I/F,<br>Indirect addressing                                   | n/a               | n/a                          | 18-bit /<br>24-bit             | WVGA                  | 64K colors   | 1024KB,<br>SRAM    | 1.35V to 1.65V | 1.62V to 3.60V | PinP  | QFP20-144<br>PFBGA10U-121  |
| S1D13781F00A                 | 8-bit / 16-bit I/F,<br>Direct addressing<br>Indirect addressing, SPI | 4-bit / 8-bit     | 8-bit /<br>16-bit            | 16-bit /<br>18-bit /<br>24-bit | WQVGA                 | MSTN:64 grayscale<br>CSTN:64K colors<br>TFT:16M colors | 384KB,<br>SRAM     | 1.35V to 1.65V | 1.62V to 3.60V | PinP,<br>a-Blend,<br>2D BitBLT              | QFP15-100                  |
| S1D13A04F00A<br>B00B         | 8-bit I/F (with external logic)<br>16-bit I/F,<br>Direct addressing  | 4-bit / 8-bit     | 4-bit /<br>8-bit /<br>16-bit |                                | QVGA                  | MSTN:64 grayscale<br>CSTN:64K colors<br>TFT:64K colors | 160KB,<br>SRAM     | 1.8V to 2.75V  | 3.0V to 3.6V   | 2D BitBLT,<br>SwivelView,<br>USB client 1.1 | TQFP15-128<br>PFBGA10U-121 |
| S1D13A05B00B                 | 8-bit I/F (with external logic)<br>16-bit I/F,<br>Direct addressing  | 4-bit / 8-bit     |                              | 9-bit /<br>12-bit /<br>18-bit  | QVGA                  | MSTN:64 grayscale<br>CSTN:64K colors<br>TFT:64K colors | 256KB,<br>SRAM     | 1.8V to 2.75V  | 3.0V to 3.6V   | 2D BitBLT,<br>SwivelView,<br>USB client 1.1 | PFBGA10U-121               |

## ■ S1D13781 Block Diagram



# ■ S1D13781 Evaluation Board (S5U13781P00C100)



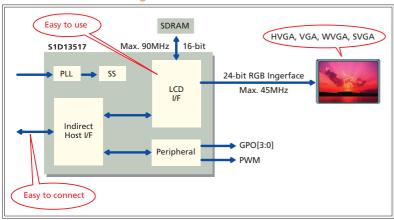
### **■ LCD Controller with External VRAM**

An LCD controller for application in a wide range of small- to large-size panel types.

This product is most suitable for the display control of OA or FA equipment operation panels as well as for in-vehicle devices.

| This produce is most suitable for the display control of or to invegal principle as well as for invertible defined. |   |                   |                              |                               |                       |  |                        |                |              |   |                         |
|---|---|-------------------|------------------------------|-------------------------------|-----------------------|--|------------------------|----------------|--------------|---|-------------------------|
|   | CPU Interface   | LCI               | ) Interface                  | Support                       |                       | Color Depth  | External               | Supply V       | oltage       | Additional                                |                         |
| Product   | Support   | Monochrome<br>STN | Color<br>STN                 | TFT                           | Typical<br>Resolution | (Max.)   | Memory<br>Capacity     | Core           | 10           | Features                                  | Package                 |
| S1D13505F00A  | 8-bit / 16-bit I/F<br>Direct addressing                                 | 4-bit / 8-bit     | 4-bit /<br>8-bit /<br>16-bit | 9-bit /<br>12-bit /<br>18-bit | VGA                   | MSTN:16 grayscale<br>CSTN:4K colors<br>TFT:64K colors    | Up to 2MB<br>EDO-RAM   | 2.7V to 5.5V   | 2.7V to 5.5V | CRT support                               | QFP15-128               |
| S1D13506F00A  | 8-bit / 16-bit I/F<br>Direct addressing                                 | 4-bit / 8-bit     | 4-bit /<br>8-bit /<br>16-bit | 9-bit /<br>12-bit /<br>18-bit | VGA                   | MSTN:64 grayscale<br>CSTN:64K colors<br>TFT:64K colors   | Up to 2MB<br>EDO-RAM   | 2.7V to 5.5V   | 2.7V to 5.5V | CRT or NTSC/<br>PAL support,<br>2D BitBLT | QFP15-128               |
| S1D13513F01A  | 16-bit I/F,<br>Direct addressing,<br>Indirect addressing,<br>Serial I/F |                   | 8-bit                        | 18-bit                        | VGA                   | MSTN:64 grayscale<br>CSTN:256K colors<br>TFT:256K colors | Up to 16MB<br>SDRAM    | 1.65V to 1.95V | 3.0V to 3.6V | 2D Sprite,<br>2D BitBLT                   | QFP22-208               |
| S1D13513B01B  | 16-bit I/F,<br>Direct addressing,<br>Indirect addressing,<br>Serial I/F | 8-bit             | 8-bit                        | 18-bit /<br>24-bit            | VGA                   | MSTN:64 grayscale<br>CSTN:256K colors<br>TFT:16M colors  | Up to 16MB<br>SDRAM    | 1.65V to 1.95V | 3.0V to 3.6V | 2D Sprite,<br>2D BitBLT                   | PBGA1UC256              |
|   | 16-bit I/F,<br>Direct addressing,<br>Indirect addressing,<br>Serial I/F | n/a               | n/a                          | 18-bit /<br>24-bit            | XGA                   | 16M colors   | Up to 64MB<br>SDRAM    | 1.65V to 1.95V | 3.0V to 3.6V | Prewarping<br>Embedded<br>RISC CPU        | QFP22-256<br>PBGA1UC256 |
| S1D13517F00A  | 8-bit /16-bit I/F<br>Indirect addressing                                | n/a               | n/a                          | 18-bit /<br>24-bit            | SVGA                  | 16M colors   | Up to<br>16MB<br>SDRAM | 2.3V to 2.7V   | 3.0V to 3.6V | PinP<br>α-Blend                           | QFP15-128               |

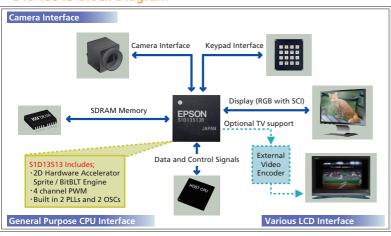
## ■ S1D13517 Block Diagram



# ■ S1D13517 Evaluation Board (S5U13517P00C100)



## ■ S1D13513 Block Diagram



# ■ S1D13513 Evaluation Board (S5U13513P00C100)



### **■ LCD Controller Supporting Camera Interface**

With the installation of a camera interface, this LCD controller can display camera images on the panel without placing load on the CPU.

This product is most suitable for the display control of a wide variety of applications such as mobile terminals and security devices

| inis product is      | This product is most suitable for the display control of a wide variety of applications such as mobile terminals and security devices. |                    |                       |  |                    |                     |         |                   |                   |                   |  |                           |  |
|----------------------|--|--------------------|-----------------------|--|--------------------|---------------------|---------|-------------------|-------------------|-------------------|--|---------------------------|--|
| Product              | CPU Interface  |                    | Interface<br>upport   | Color Depth  | Internal<br>Memory | External<br>Memory  | Camera  |                   | Supply            | Voltage           | Additional                             | Package                   |  |
| Floduct              | Support  | TFT                | Typical<br>Resolution | (Max.)   | Capacity           |                     | (pixel) | Codec             | Core              | 10                | Features                               |                           |  |
| S1D13513F01A         | 16-bit I/F,<br>Direct addressing,<br>Indirect addressing,<br>Serial I/F  | 18-bit             | VGA                   | MSTN:64 grayscale<br>CSTN:256K colors<br>TFT:256K colors | 0                  | Up to 16MB<br>SDRAM | 0.3MP   | n/a               | 1.65V to<br>1.95V | 3.0V to 3.6V      | 2D Sprite,<br>2D BitBLT                | QFP22-208                 |  |
| S1D13513B01B         | 16-bit I/F, Direct addressing, Indirect addressing, Serial I/F   | 18-bit /<br>24-bit | VGA                   | MSTN:64 grayscale<br>CSTN:256K colors<br>TFT:16M colors  | 0                  | Up to 64MB<br>SDRAM | 0.3MP   | n/a               | 1.65V to<br>1.95V | 3.0V to 3.6V      | 2D Sprite,<br>2D BitBLT                | PBGA1UC256                |  |
| S1D13515F00A<br>B00B | 16-bit I/F, Direct addressing, Indirect addressing, Serial I/F   | 18-bit /<br>24-bit | XGA                   | 16M colors   | 0                  | Up to 64MB<br>SDRAM | 0.3MP   | n/a               | 1.65V to<br>1.95V | 3.0V to 3.6V      | Prewarping<br>Embedded<br>RISC CPU     | QFP22-256<br>PBGA1UC256   |  |
| S1D13717F00A<br>B00B | 16-bit I/F,<br>Direct addressing,<br>Indirect addressing,<br>Serial port I/F for LCD   | 18-bit             | 176x<br>220           | 64K colors   | 224KB,<br>SRAM     | n/a                 | 0.3MP   | Encode/<br>Decode | 1.65V to<br>1.95V | 2.75V to<br>3.25V | SD memory<br>card I/F                  | QFP21-176<br>PFBGA12U-180 |  |
| S1D13715F01A<br>B00B | 16-bit I/F,<br>Direct addressing,<br>Indirect addressing,<br>Serial port I/F for LCD   | 18-bit /<br>24-bit | QVGA                  | 16M colors   | 320KB,<br>SRAM     | n/a                 | 1.3MP   | Encode/<br>Decode | 1.65V to<br>1.95V | 2.75V to<br>3.25V | 2D BitBLT,<br>SwivelView               | QFP21-176<br>PFBGA10U-160 |  |
| S1D13719B00B         | 16-bit I/F,<br>Direct addressing,<br>Indirect addressing   | 18-bit /<br>24-bit | QVGA                  | 16M colors   | 512KB,<br>SRAM     | n/a                 | 2.0MP   | Encode/<br>Decode | 1.65V to<br>1.95V | 2.30V to<br>3.25V | 2D BitBLT,<br>SwivelView,<br>SD memory | PFBGA10U-180              |  |

Supply

card I/F

card I/F

### ■ In-vehicle LCD Controller In our line-up of display controller products, this controller complies with in-vehicle quality.

**LCD Interface Support** 

| Product              | CPU Interface  | LCD               | nterfac      | e Supp             | ort                   | Color<br>Depth              | Internal       | External               | Camera  | JPEG              | Volt                 |    | Temperature   | Additional                             | Package                 |
|----------------------|--|-------------------|--------------|--------------------|-----------------------|-----------------------------|----------------|------------------------|---------|-------------------|----------------------|----|---------------|--|-------------------------|
| Product              | Support  | Monochrome<br>STN | Color<br>STN | TFT                | Typical<br>Resolution | (Max.)                      | wielliol y     | Memory<br>Capacity     | (nivol) | Codec             | Core                 | 10 | Range         | features                               | rackage                 |
| S2D13513F01A         | 16-bit I/F,<br>Direct addressing,<br>Indirect addressing,<br>Serial I/F, | 8-bit             | 8-bit        | 18-bit             | VGA                   | 64 grayscale<br>256K colors | 0              | Up to<br>16MB<br>SDRAM | 0.3MP   | n/a               | 1.65V<br>to<br>1.95V | to | -40 to +105°C | 2D Sprite,<br>2D BitBLT                | QFP22-208               |
| S2D13513B01B         | 16-bit I/F,<br>Direct addressing,<br>Indirect addressing,<br>Serial I/F, | 8-bit             | 8-bit        | 18-bit /<br>24-bit | VGA                   | 64 grayscale<br>16M colors  | 0              | Up to<br>64MB<br>SDRAM | 0.3MP   | n/a               | 1.65V<br>to<br>1.95V | to | -40 to +105°C | 2D Sprite,<br>2D BitBLT                | PBGA1UC256              |
| S2D13515F00A<br>B00B | 16-bit I/F,<br>Direct addressing,<br>Indirect addressing,<br>Serial I/F, | n/a               | n/a          | 18-bit /<br>24-bit | XGA                   | 16M colors                  | 0              | Up to<br>64MB<br>SDRAM | 0.3MP   | n/a               | 1.65V<br>to<br>1.95V | to | -40 to +105°C | Prewarping<br>Embedded<br>RISC CPU     | QFP22-256<br>PBGA1UC256 |
| S2D13719F00A         | 16-bit I/F,<br>Direct addressing,<br>Indirect addressing                 | n/a               | n/a          | 18-bit /<br>24-bit | QVGA                  | 16M colors                  | 512KB,<br>SRAM | n/a                    | 2.0MP   | Encode/<br>Decode | 1.65V<br>to<br>1.95V | to | -40 to +105°C | 2D BitBLT,<br>SwivelView,<br>SD memory | QFP22-208               |

Internal External

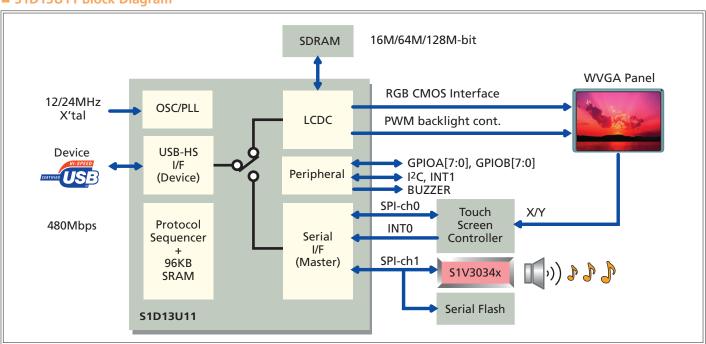
Color

### ■ USB Interface LCD Controller

An LCD controller allowing for reception of display data and transmission of touch-screen coordinate data at high speed via USB2.0-HS. Most suitable for application to OA equipment such as multi-functional printers with long lengths of cabling between the host CPU and LCD panel, and to in-vehicle devices such as rear entertainment displays.

| Product      | CPU<br>Interface<br>Support |                   | LCD Interface Support |                 |                       |            | Internal |                    | Supply \       | /oltage      | ۸ ماما: <u>۱</u> م م م ا |           |
|--------------|-----------------------------|-------------------|-----------------------|-----------------|-----------------------|------------|----------|--------------------|----------------|--------------|--------------------------|-----------|
|              |                             | Monochrome<br>STN | Color STN             | TFT             | Typical<br>Resolution |            | -        | Memory<br>Capacity | Core           | 10           | Additional<br>Features   | Package   |
| S1D13U11F00A | USB2.0 HS                   | n/a               | n/a                   | 18-bit / 24-bit | SVGA                  | 16M colors | 0        | Up to 16MB         | 1.65V to 1.95V | 3.0V to 3.6V | PinP<br>α-Blend          | QFP20-144 |

### ■ S1D13U11 Block Diagram



# ■ S1D13U11 Evaluation Board (S5U13U11P10C100)



# ■ S1D13U11 Evaluation Board (S5U13U11P00C100)



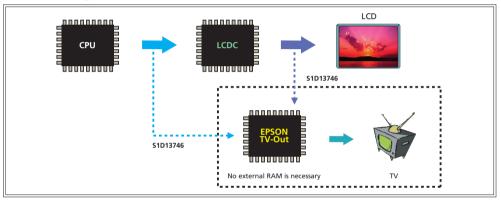
### **■ Video Encoder**

A video encoder with built-in VRAM, able to be connected directly with the host CPU bus and TFT panel interface.

This product is most suitable for various applications that display images on televisions, because it supports a wide array of input formats and complies with NTSC/PAL standards.

|                      | CPU Interface  | TV                     | TV  | Input Dat                        |                |                | External             | Supply            | Volta               | ge              | Additional                                    |                          |
|----------------------|--|------------------------|---|----------------------------------|----------------|----------------|----------------------|-------------------|---------------------|-----------------|---|--------------------------|
| Product              | Support  | Output                 | Standard                                    | RGB                              |                |                | Memory<br>Capacity   | Core              | Ю                   | DAC             | Features                                      | Package                  |
| S1D13506F00A         | 8-bit / 16-bit I/F<br>Direct addressing  | Composite<br>/ S-video | PAL<br>NTSC                                 | 5:6:5<br>8bpp<br>4bpp            | n/a            | 0              | Up to 2MB<br>EDO-RAM | 2.7V to 5.5V      | 2.7V to<br>5.5V     | 2.7V to<br>5.5V | LCD support<br>2D BitBLT                      | QFP15-128                |
| S1D13746F01A<br>B01B | 8-bit / 16-bit I/F<br>Direct addressing,<br>Serial I/F<br>(only for register access) | Composite / S-video    | PAL:B, D, G,<br>H, I, M, N, Nc<br>NTSC:M, J | 8:8:8<br>6:6:6<br>5:6:5<br>3:3:2 | 4:2:2<br>4:2:0 | 312KB,<br>SRAM | n/a                  | 1.35V to<br>1.65V | 1.62V<br>to<br>3.6V | 2.7V to<br>3.3V | SwivelView,<br>Image<br>Enhancement<br>Engine | QFP15-128<br>PFBGA7U-100 |

## ■ S1D13746 System Block Diagram



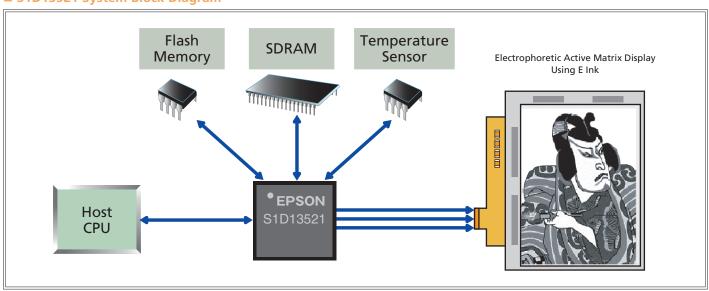
## **■ EPD Controller**

A controller IC dedicated to the electronic paper product from E Ink Corporation.

This is an epoch-making product equipped with an architecture that maximizes the characteristics of electronic paper, allowing for pen input, text input, and other features that have thus far been difficult to achieve.

| Product      | CPU Interface  | Built-in | External                             | Maximum<br>Resolution | Gravesala                              | Supply         | Voltage                          | Additional Features  | Package                     |
|--------------|--|----------|--------------------------------------|-----------------------|--|----------------|----------------------------------|--|-----------------------------|
| Product      | Support  | Memory   | Memory                               | Resolution            | Grayscale                              | Core           | 10                               | Additional reatures  | rackage                     |
| S1D13521B01B | 16-bit I/F<br>Indirect addressing                                  | -        | SDRAM I/F<br>(for frame buffer)      | 4096 x 4096           | 2/3/4/5bpp                             | 1.65V to 1.95V | 1.65V to 1.95V<br>2.70V to 3.60V | 16 area partial update   | PFBGA8U-181<br>PFBGA12U-180 |
| S1D13522A00B | 16-bit I/F<br>Indirect addressing<br>or Serial I/F                 | 2MB      | -                                    | 1024 x 768            | 2/3/4bpp                               | 1.65V to 1.95V | 1.65V to 3.60V                   | 15 area partial update,<br>Picture in Picture<br>with Transparency | PFBGA10U-144                |
| S1D13524B01B | 16-bit TFT I/F, 16-bit I/F<br>Indirect addressing<br>or Serial I/F |          | Mobile DDR I/F<br>(For frame buffer) | 2560 x 2048           | Color:RGB565<br>Gray Scale:<br>to 4bpp |                | 1.65V to 3.60V                   | Color Processor,<br>15 area partial update                         | PFBGA11UK-241               |

## ■ S1D13521 System Block Diagram



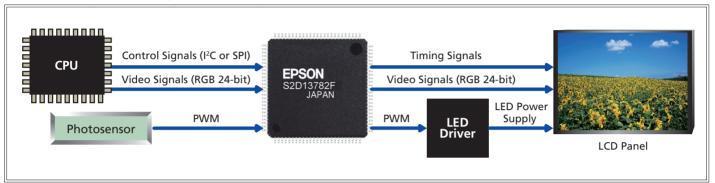
### ■ Image Enhancement IC

This is the image correction IC, which has been developed based on the Epson's unique image enhancement technologies, for medium- and small-size liquid crystal display panels.

The Epson's image correction technologies, developed for cellular phone display controllers, have been improved to the Auto Movie Enhancement 2 Extended (AME2EX) technologies. They can realize both the small power consumption of LCD panel and the colorful image display.

| Product      | Supported CPU                          | Image I/O Format   | Operating     | Power \        | Package                        |           |
|--------------|--|--|---------------|----------------|--------------------------------|-----------|
|              | Interface                              | illiage I/O Format   | Temperature   | Core           | 10                             | rackage   |
| S2D13782F00A | I <sup>2</sup> C<br>SPI (3 or 4-wired) | RGB-16-bit/18-bit/24-bit,<br>YUV-16-bit,YUV-8-bit (ITU-R BT.656) | -40 to +105°C | 1.65V to 1.95V | 1.65V to 1.95V<br>3.0V to 3.6V | QFP15-100 |

### **■ S2D13782 System Configuration Example**



### **■ Image Expression Enhancement Examples**

The AME2EX technologies can reduce the backlight power consumption but enhance the texture and vividness of dark picture.



Before Correction (at 100% Backlight Level)

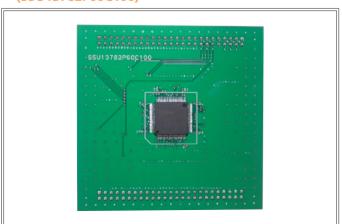


Before Correction (at 50% backlight level)



After Correction (at 50% Backlight Level)

# ■ S2D13782 Evaluation Board (S5U13782P00C100)



## ■ S2D13782 Motherboard (S5U13A00P00C100)



#### ■ In-vehicle Multi-Camera Interface IC

I<sup>2</sup>C

I/P converter

Image synthesis / Automatic change

S2S65P10F00A

An interface IC that synthesizes input images from multi-channel analog cameras and outputs the images. This product complies with in-vehicle quality requirements. Most suitable for security-related applications such as monitoring cameras and in-vehicle

| camera systems.              |                         |   |                |                |              |                           |  |  |  |  |  |
|------------------------------|-------------------------|---|----------------|----------------|--------------|---------------------------|--|--|--|--|--|
| Product                      | Host CPU Interface      | Function  | Temperature    | Supply         | Package      |                           |  |  |  |  |  |
| Product Host CPO Interface   |                         | Function  | Range          | Core           | IO / Analog  | rackage                   |  |  |  |  |  |
| S2D13P04F00A<br>S2D13P04B00B | I <sup>2</sup> C<br>SPI | Including four channels of NTSC/PAL decoders<br>8-bit digital output (supporting ITU-R BT656)<br>Equipped with multi-image synthesis mode | -40°C to +85°C | 1.65V to 1.95V | 3.0V to 3.6V | QFP15-100<br>PFBGA10U-121 |  |  |  |  |  |

| Product                      | Host CPU Interface      | Function  | Range          | Core           | IO / Analog  | Package                   |
|------------------------------|-------------------------|---|----------------|----------------|--------------|---------------------------|
| S2D13P04F00A<br>S2D13P04B00B | I <sup>2</sup> C<br>SPI | Including four channels of NTSC/PAL decoders<br>8-bit digital output (supporting ITU-R BT656)<br>Equipped with multi-image synthesis mode<br>Distortion correction function | -40°C to +85°C | 1.65V to 1.95V | 3.0V to 3.6V | QFP15-100<br>PFBGA10U-121 |
|                              |                         | Camera I/F x 4  |                |                |              |                           |

1.65V to 1.95V

2.4V to 3.6V

-40°C to +105°C

QFP15-100